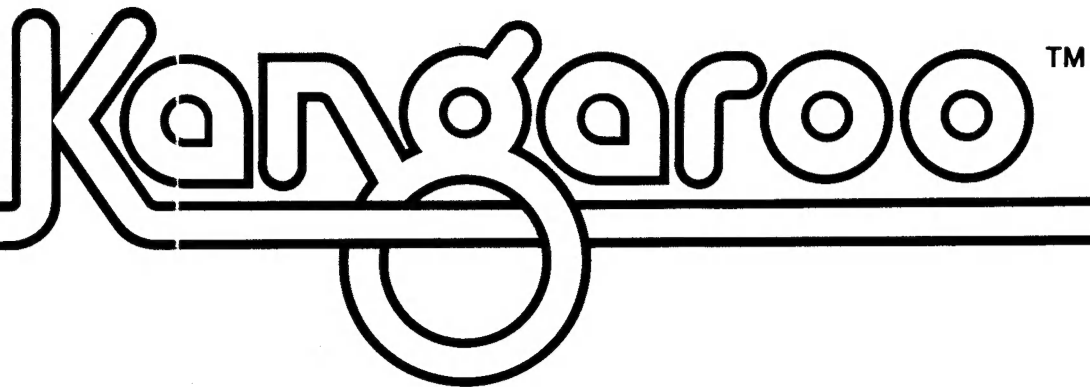


Schematic Package Supplement to



Operation, Maintenance and Service Manual

NOTE

This staple temporarily holds the schematic package together. Remove the staple before using the schematics.



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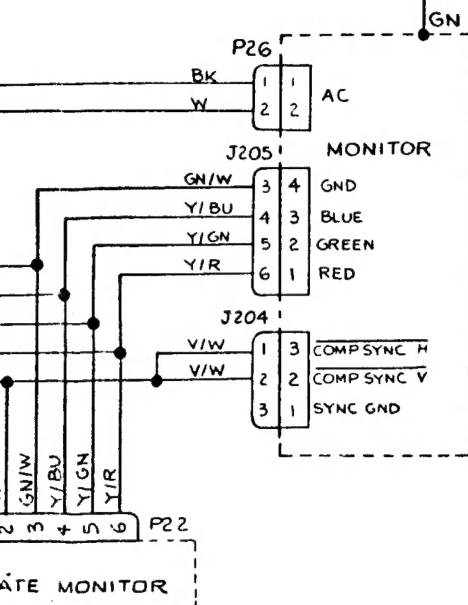
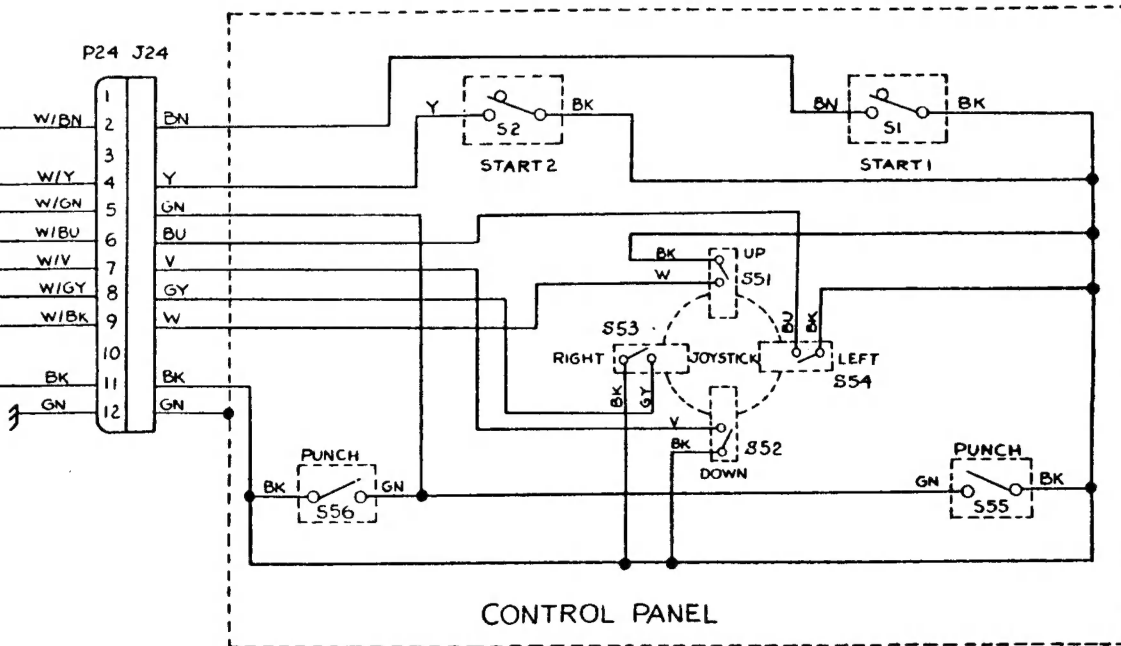
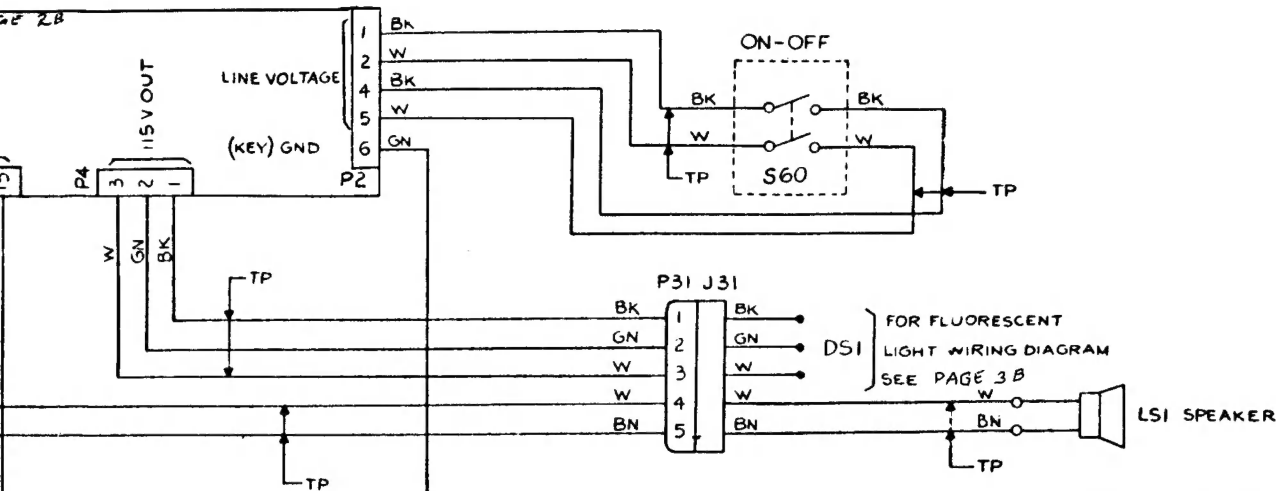
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Sheet 2B	Color Raster-Scan Power Supply Wiring Diagram (037669-01 C)
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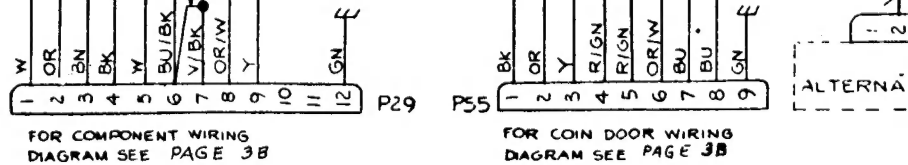
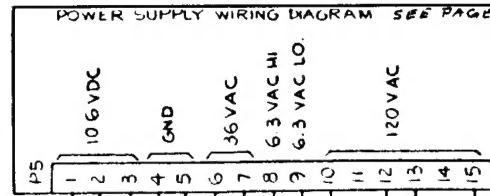
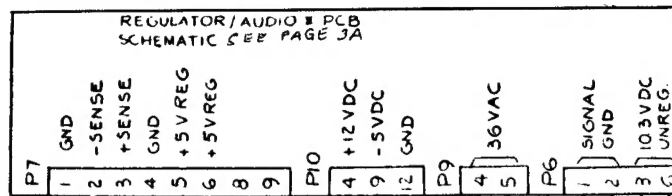
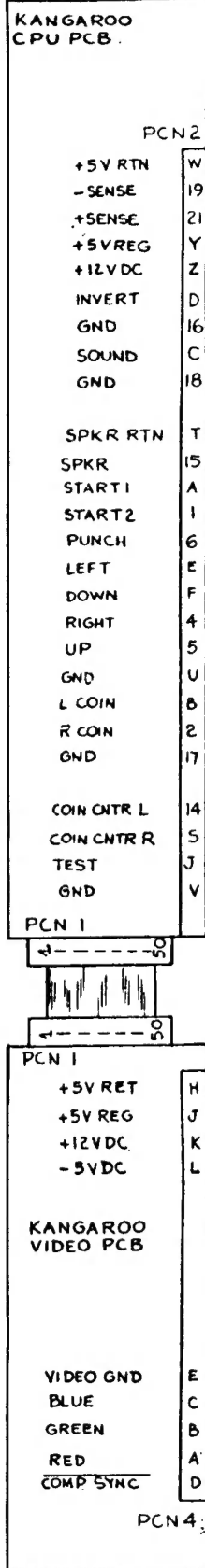
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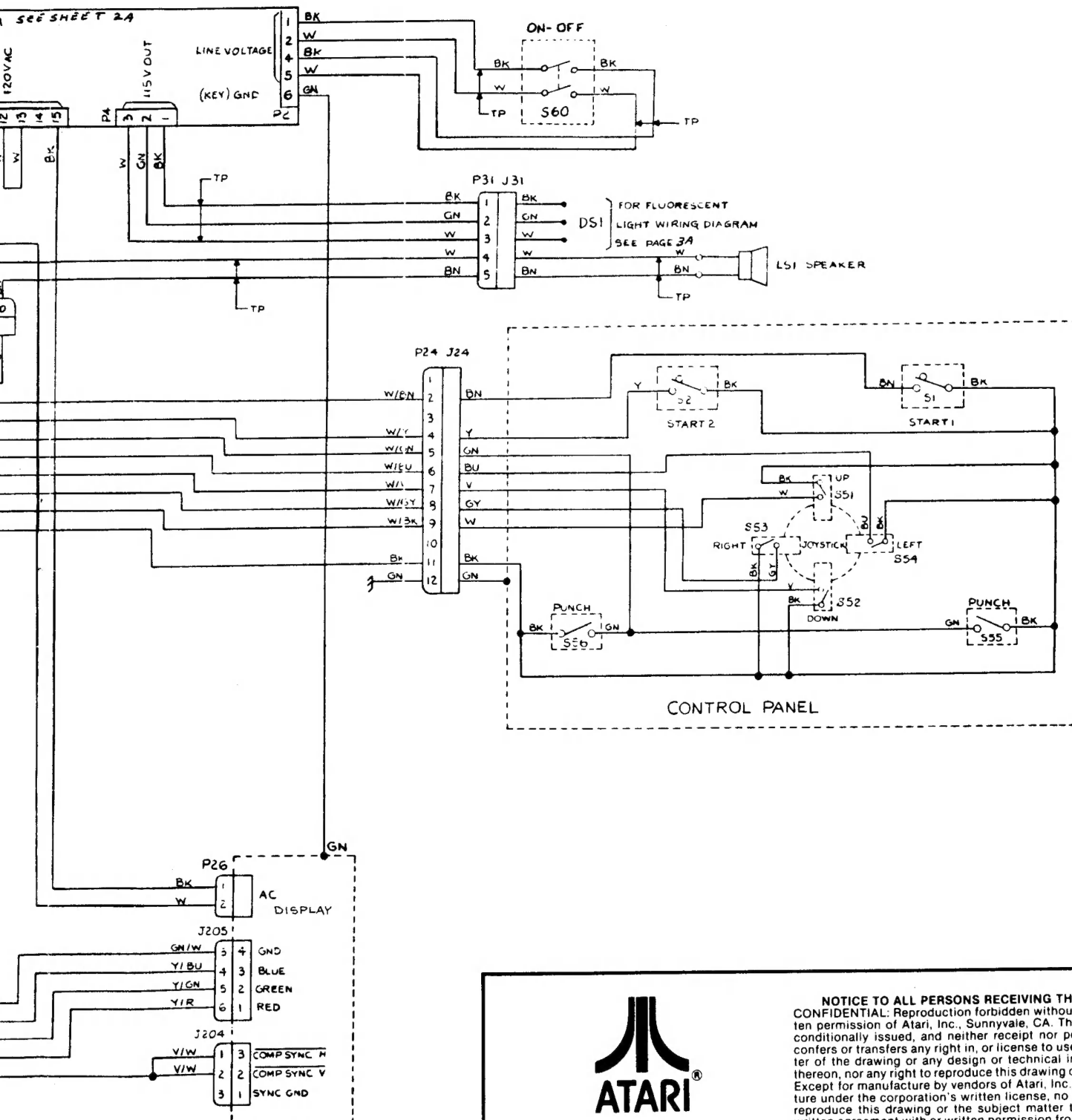
Kangaroo Upright Game Wiring Diagram

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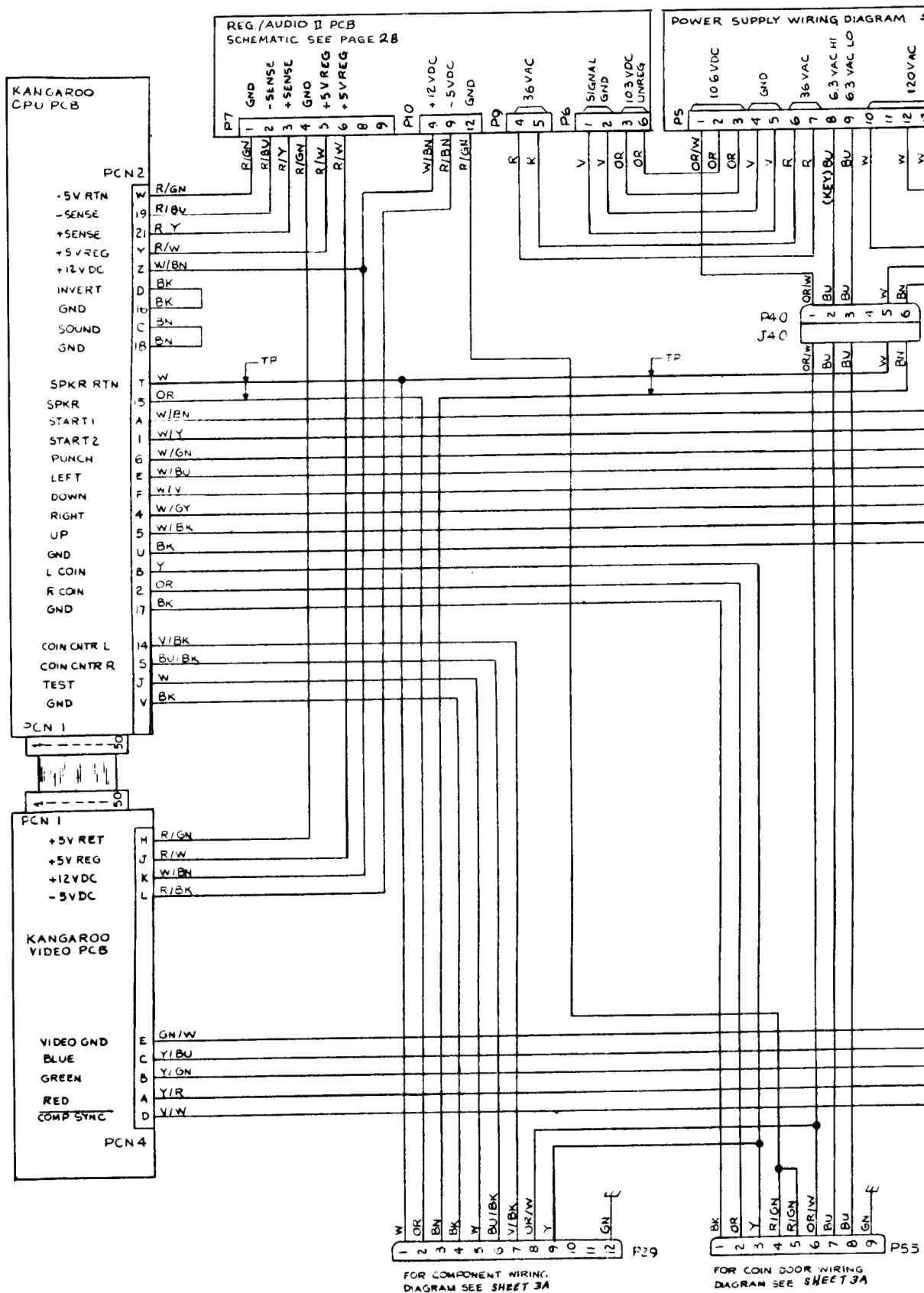
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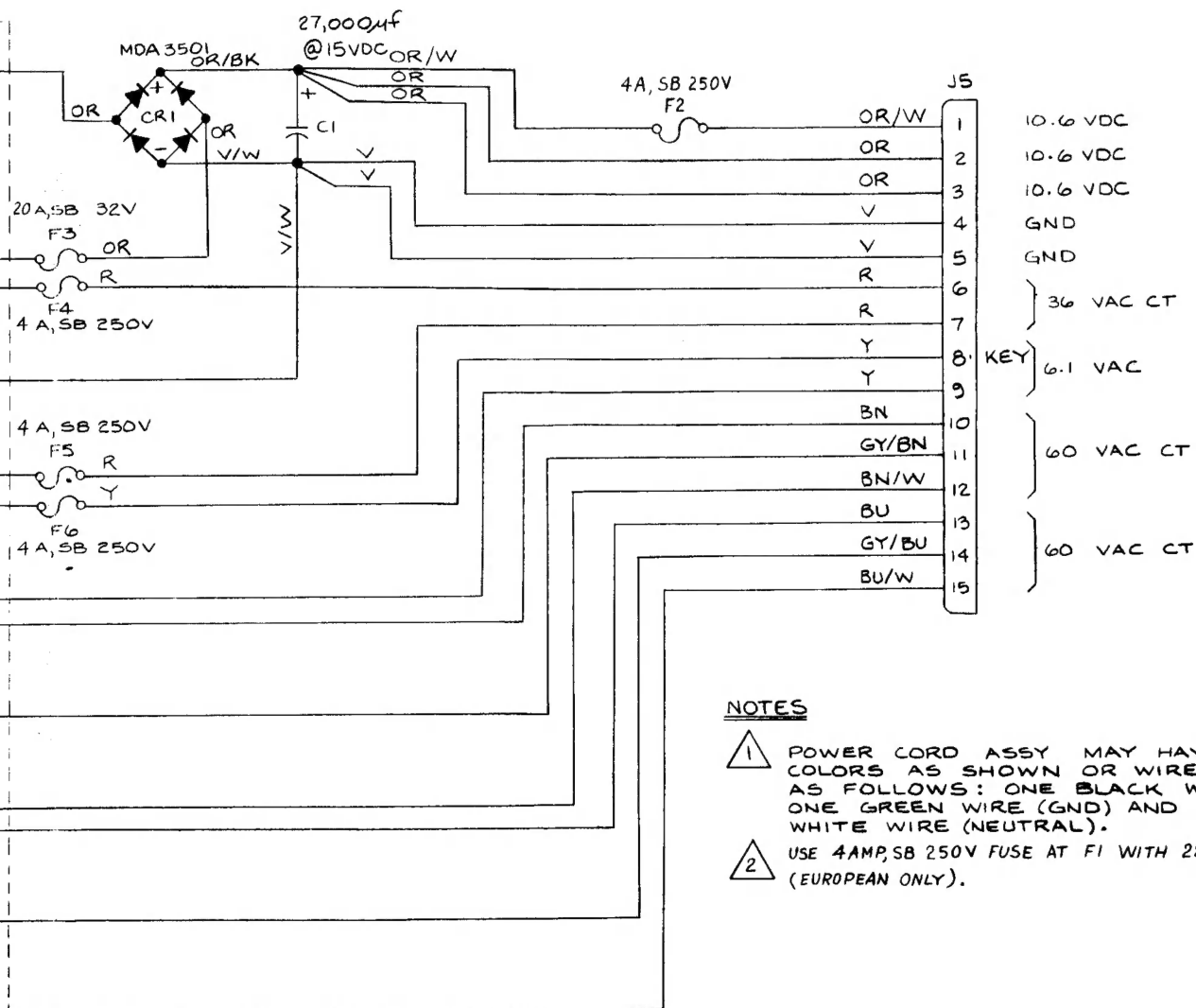
Ireland-Built Kangaroo Game Wiring Diagram

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NOTES

- 1 POWER CORD ASSY MAY HAVE WIRE COLORS AS SHOWN OR WIRE COLORS AS FOLLOWS: ONE BLACK WIRE (AC), ONE GREEN WIRE (GND) AND ONE WHITE WIRE (NEUTRAL).
- 2 USE 4AMP, SB 250V FUSE AT F1 WITH 220V & 240V (EUROPEAN ONLY).



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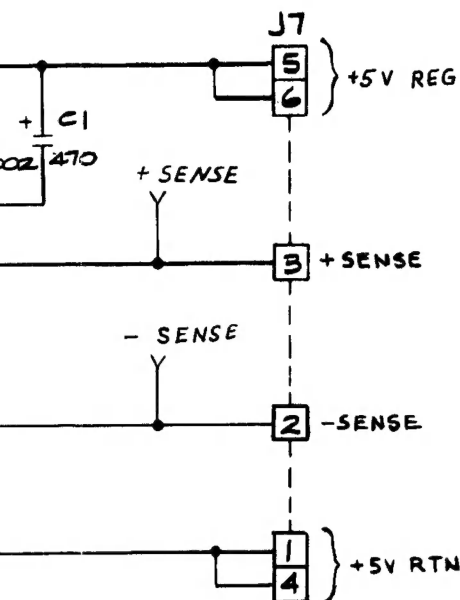
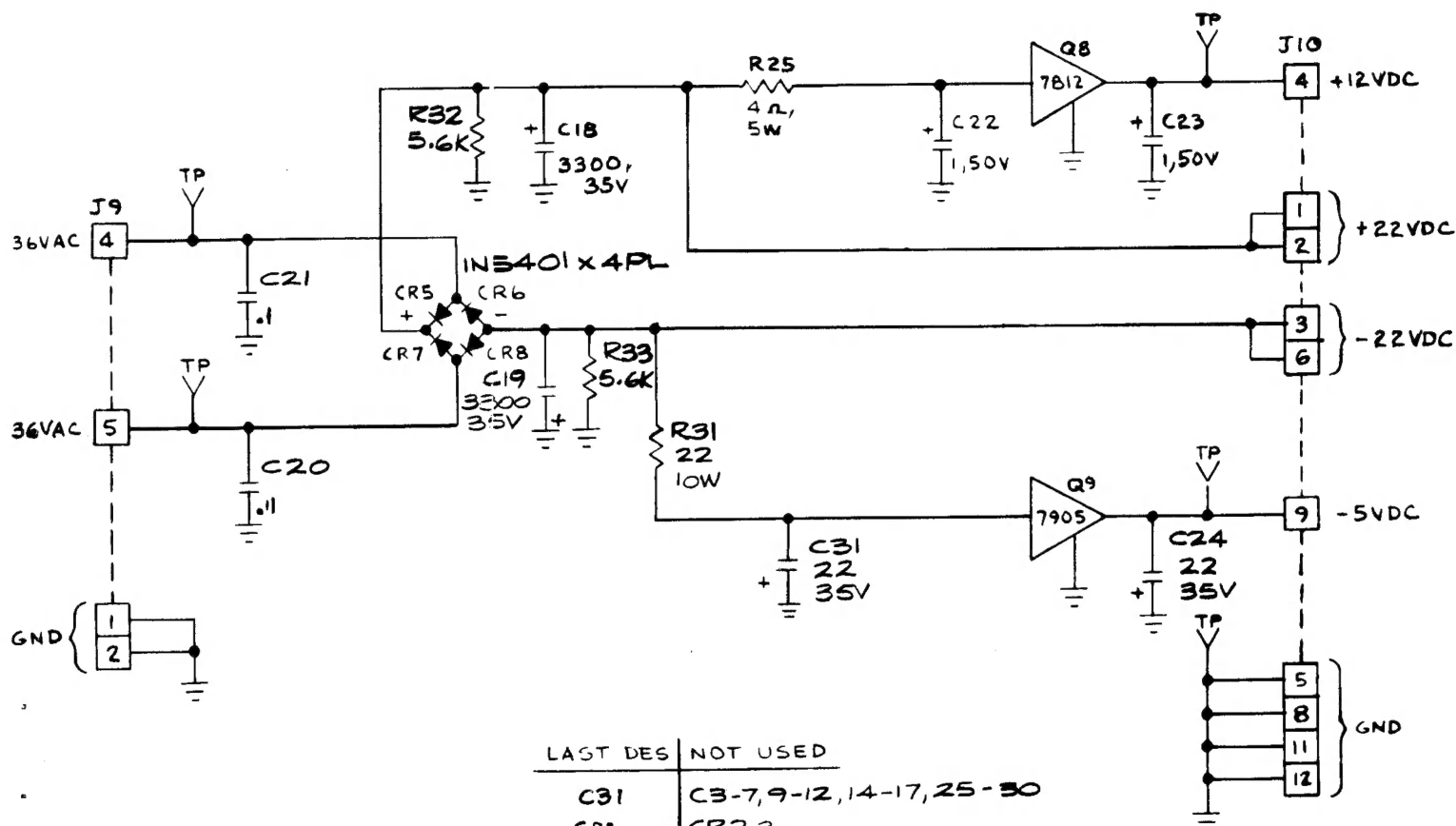
Kangaroo Power Supply

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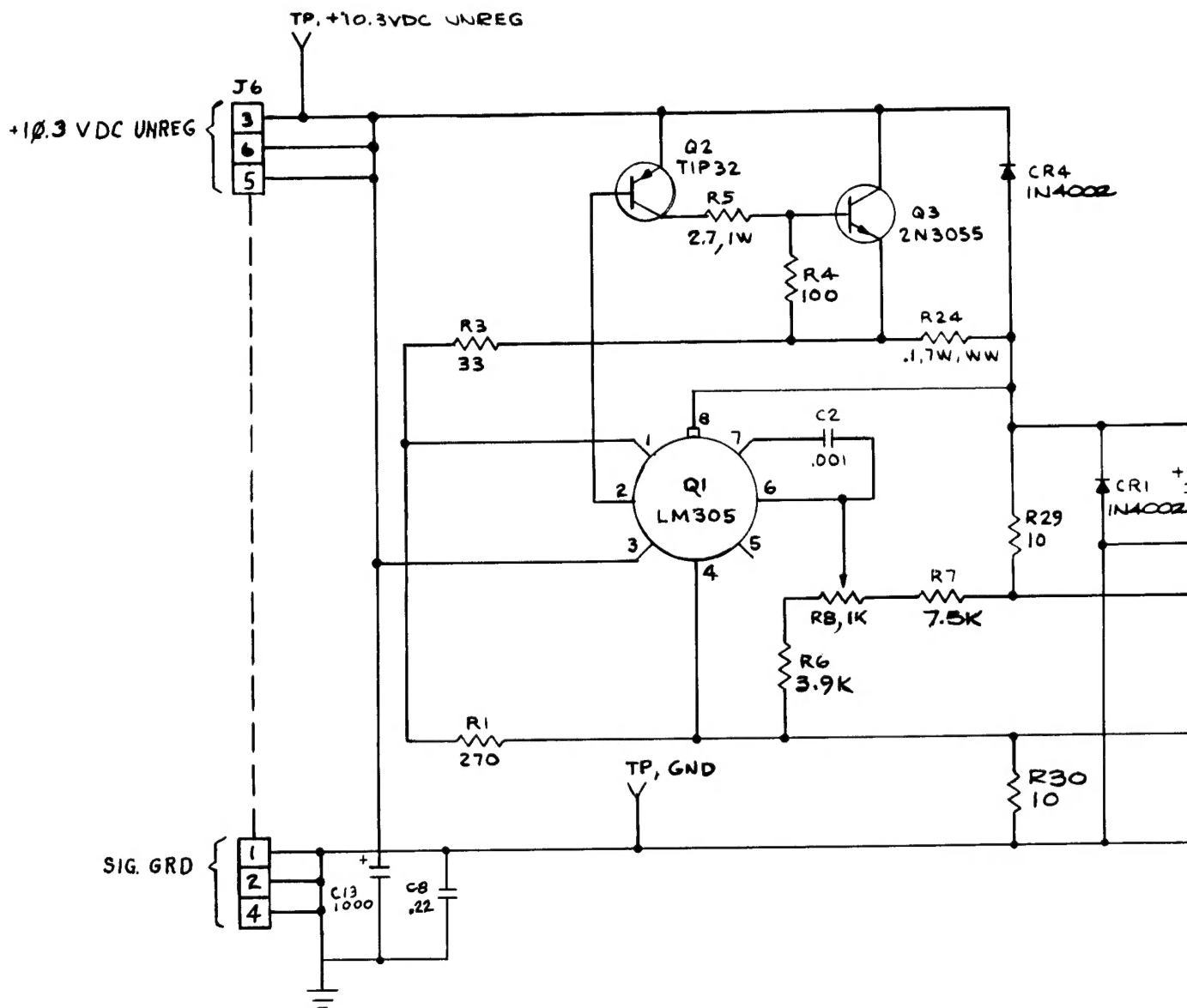
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Kangaroo Reg/Audio II PCB Schematic Diagram

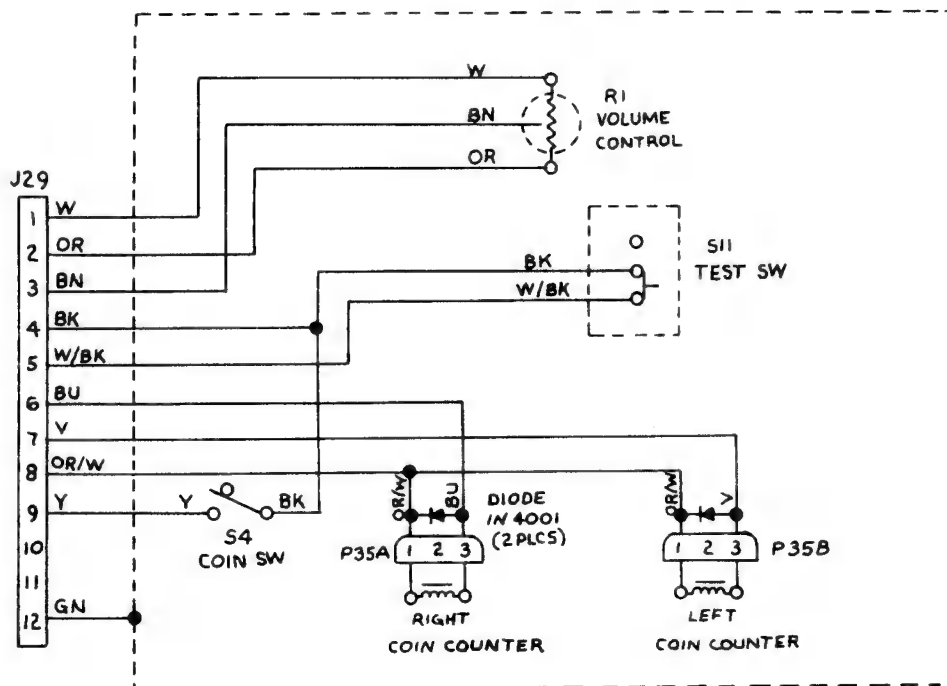
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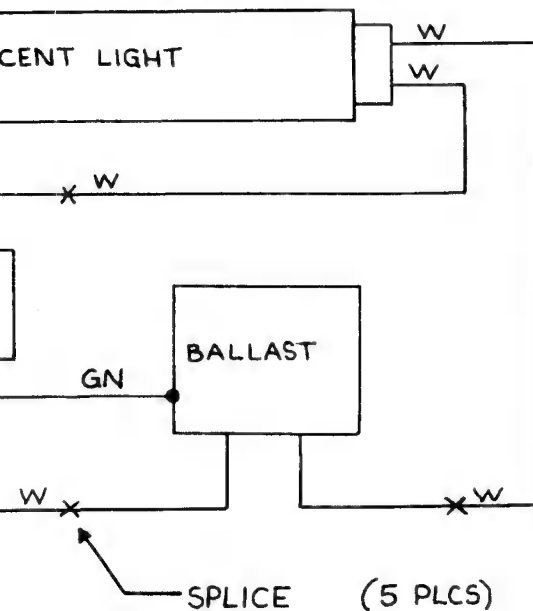
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Utility Panel Wiring Diagram



Speaker Wiring Diagram



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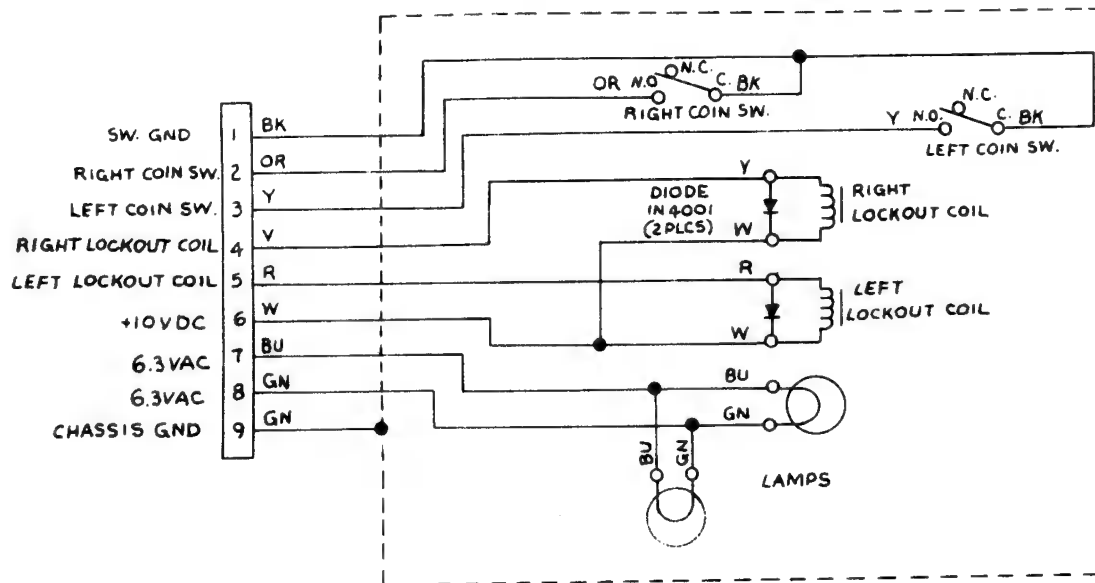
Kangaroo Game Wiring Interfaces

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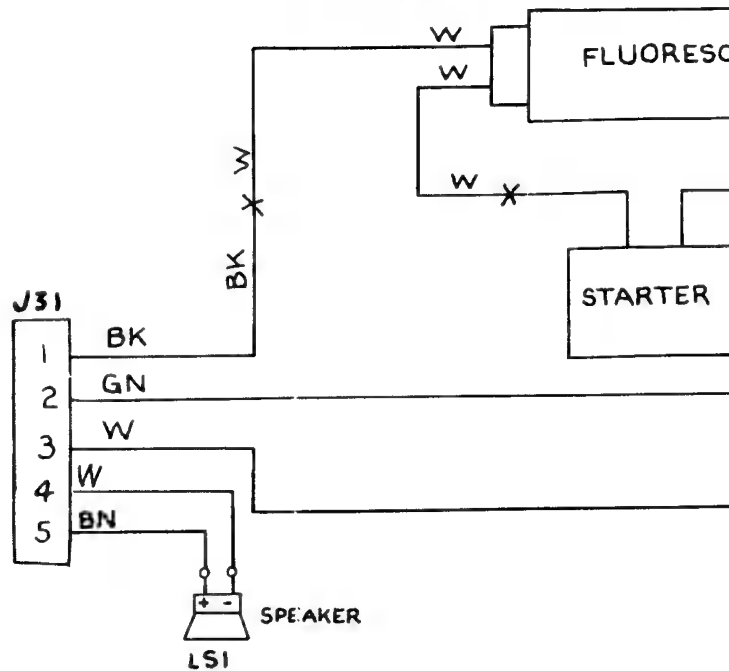
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Coin Door Wiring Diagram

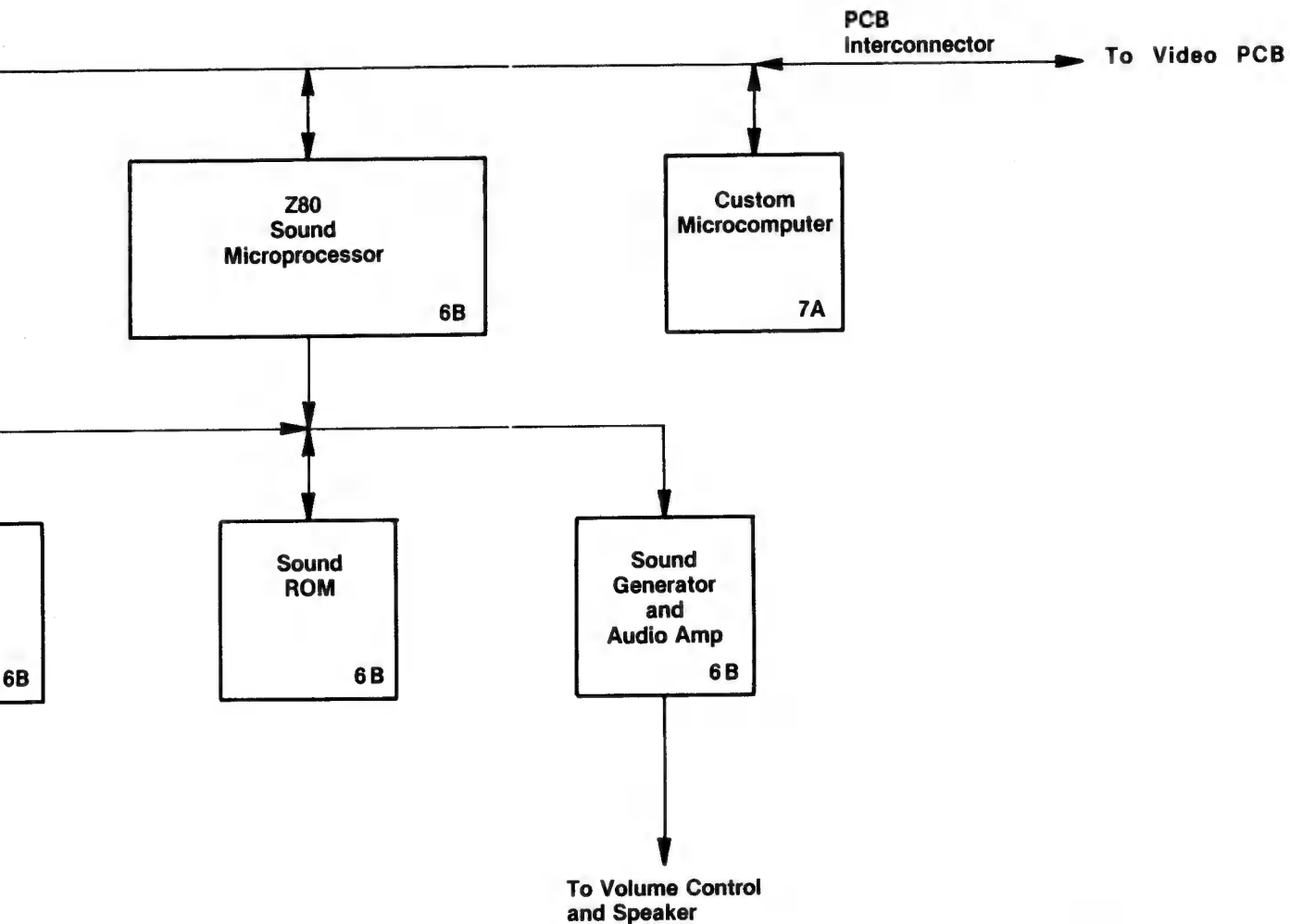


Fluorescent Light and



NOTE

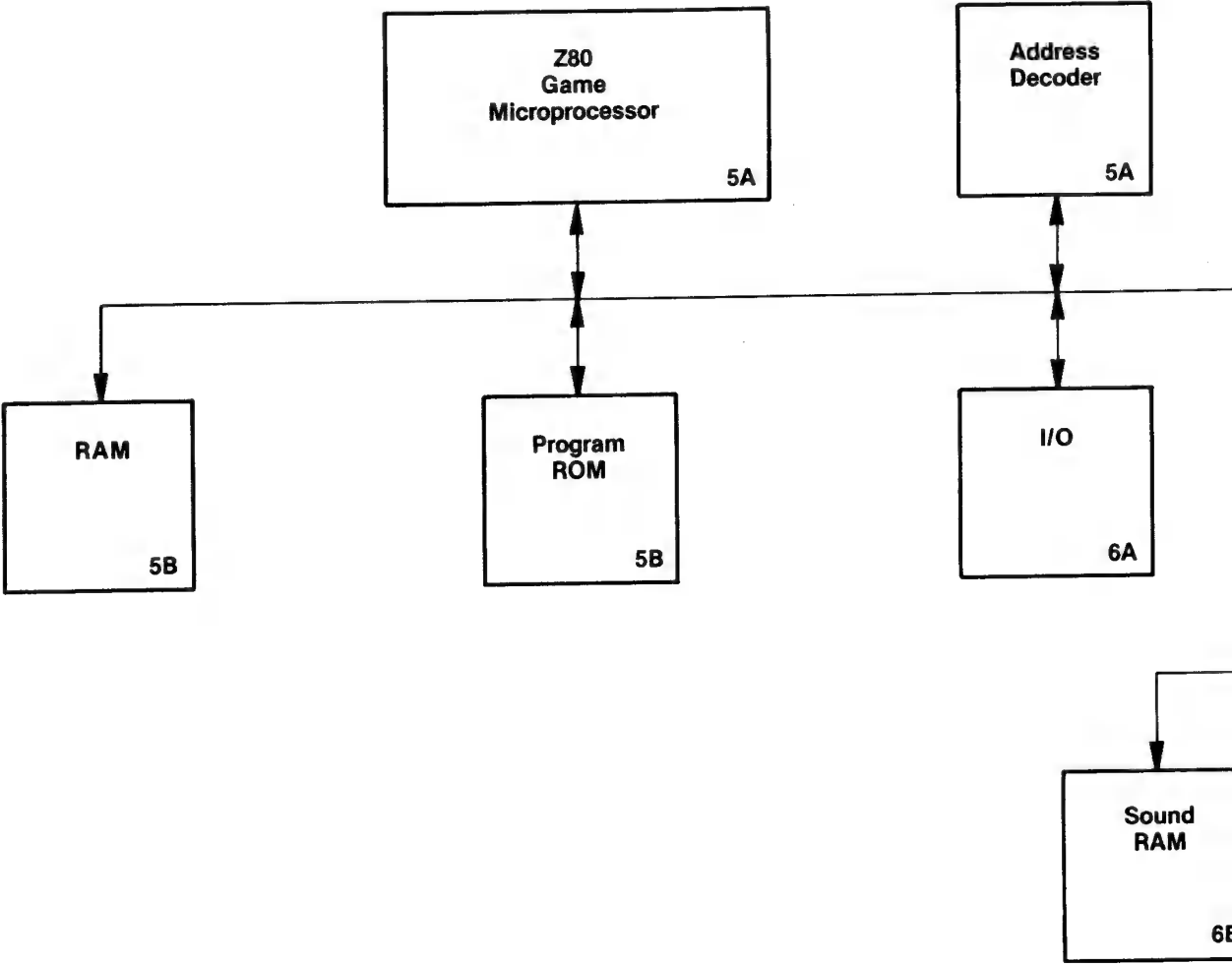
The **service switch**, located on the CPU PCB, allows you to enter credits without tripping the coin counter.



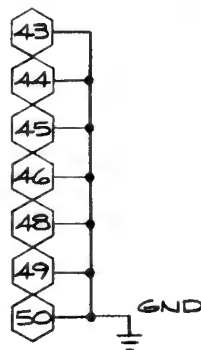
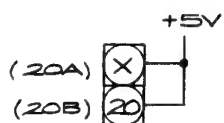
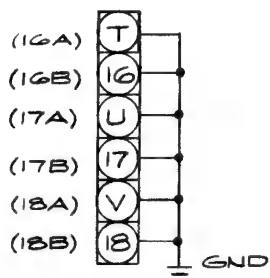
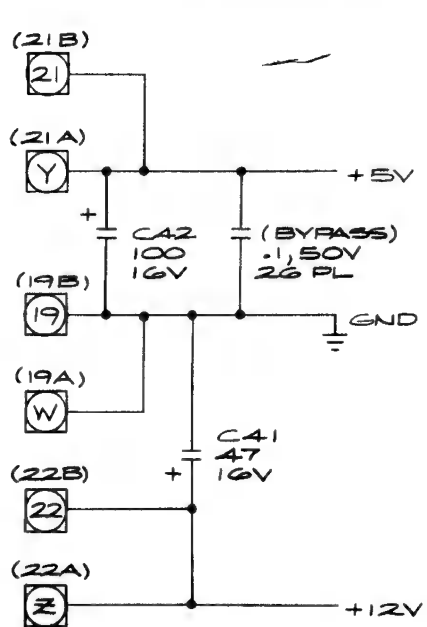
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Kangaroo CPU PCB Schematic Diagram

Block Diagram (CPU PCB)



CPU Power Input



NOTES:

1.  = CONNECTOR CN2
2.  = CONNECTOR CN1



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Kangaroo CPU PCB Schematic Diagram

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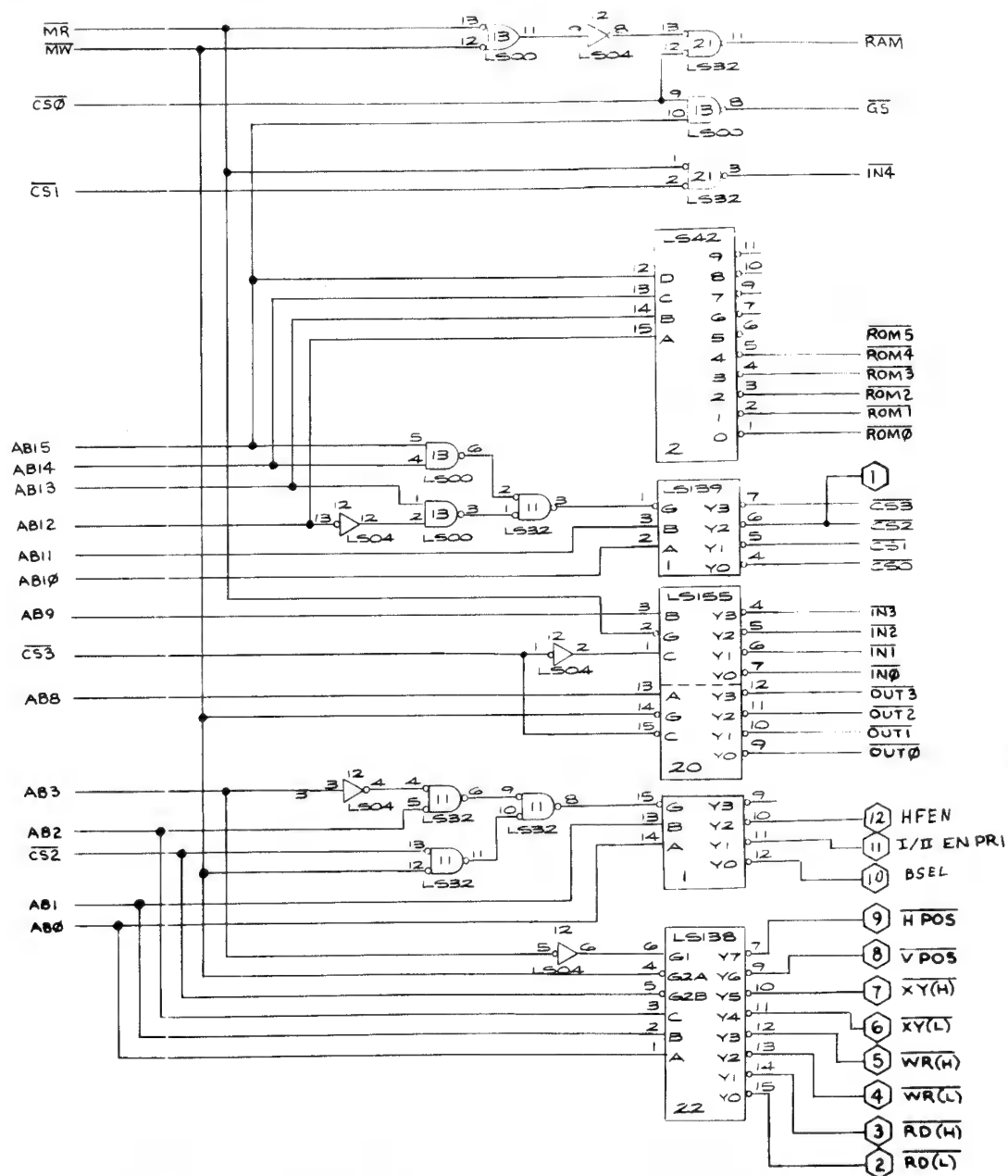
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 3rd printing

Memory Map

[illegible]

Address Decoder



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Kangaroo CPU PCB Schematic Diagram

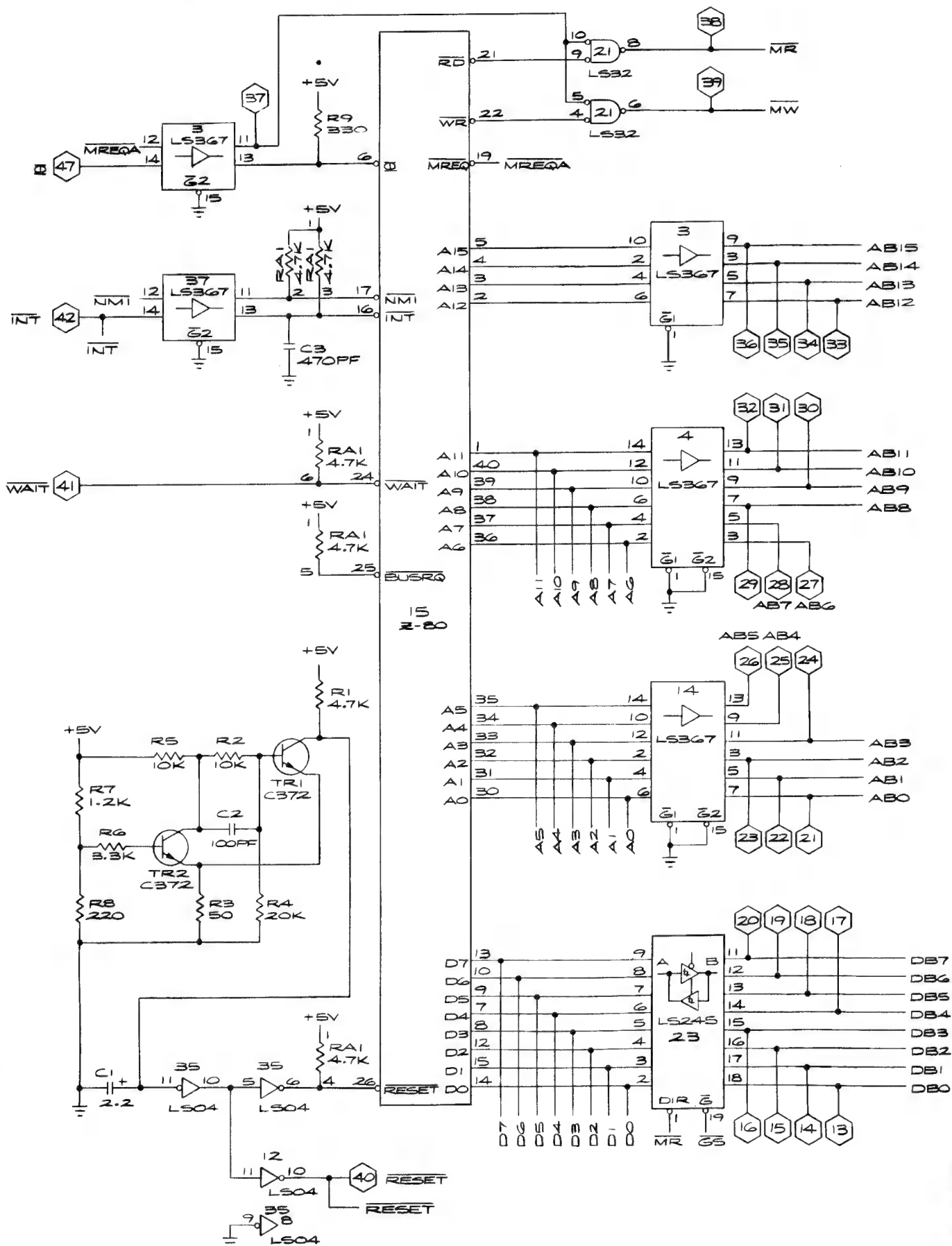
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Game Microprocessor





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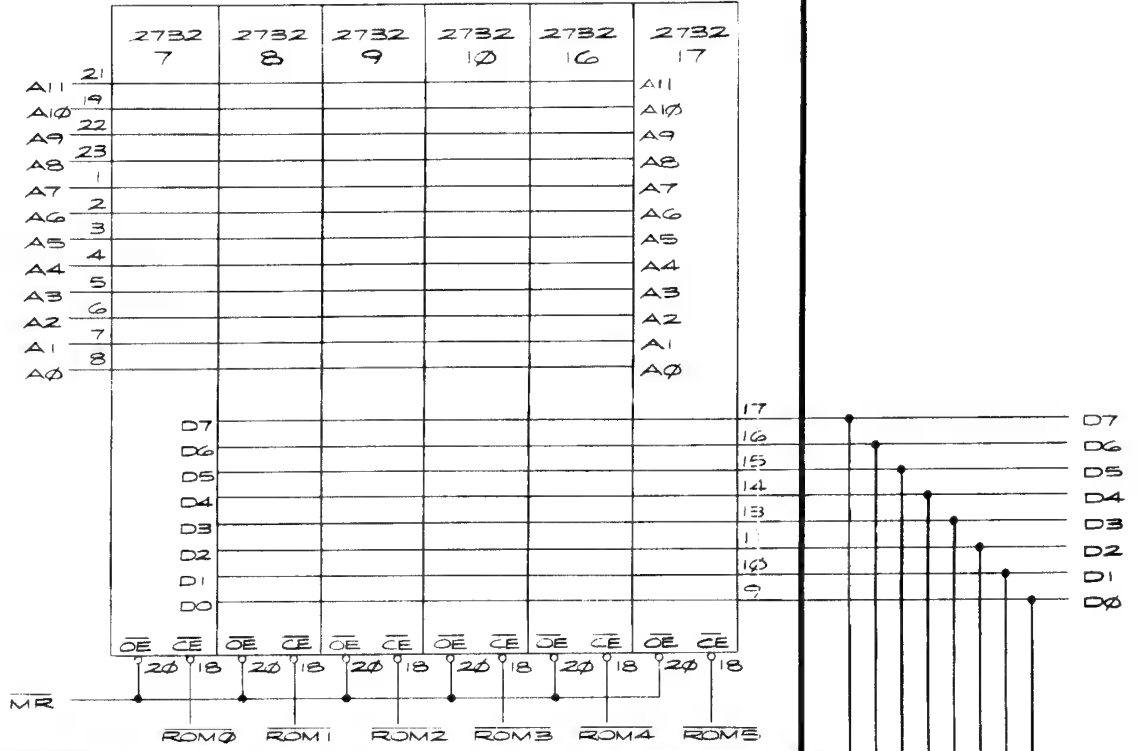
Kangaroo CPU PCB Schematic Diagram

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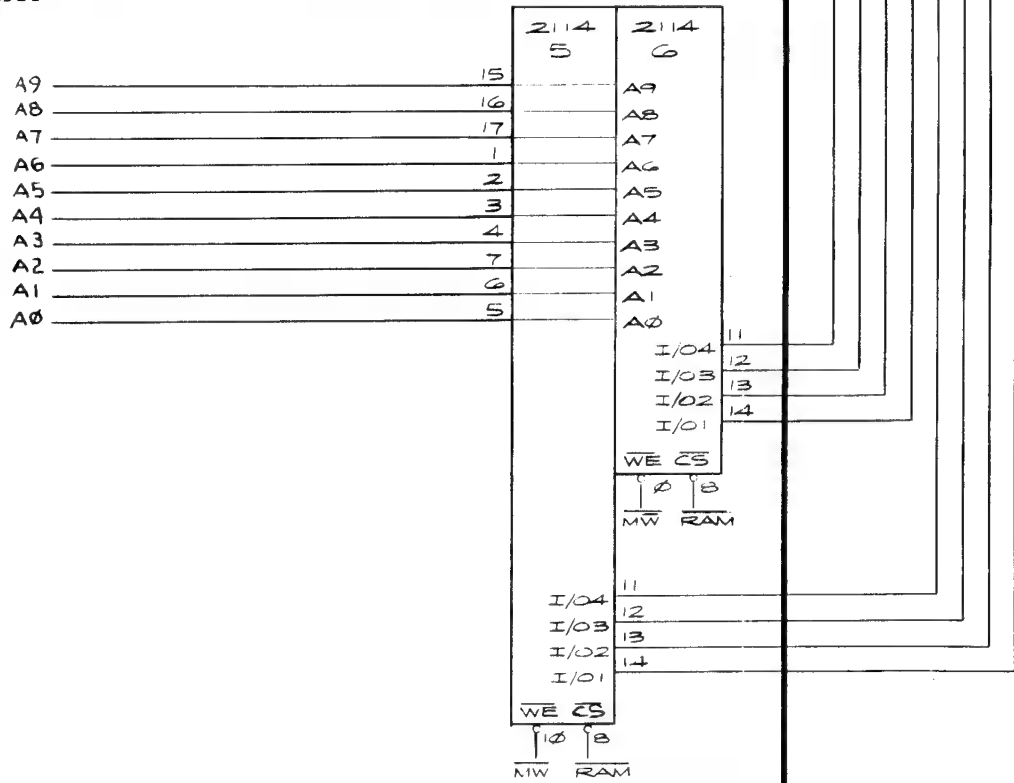
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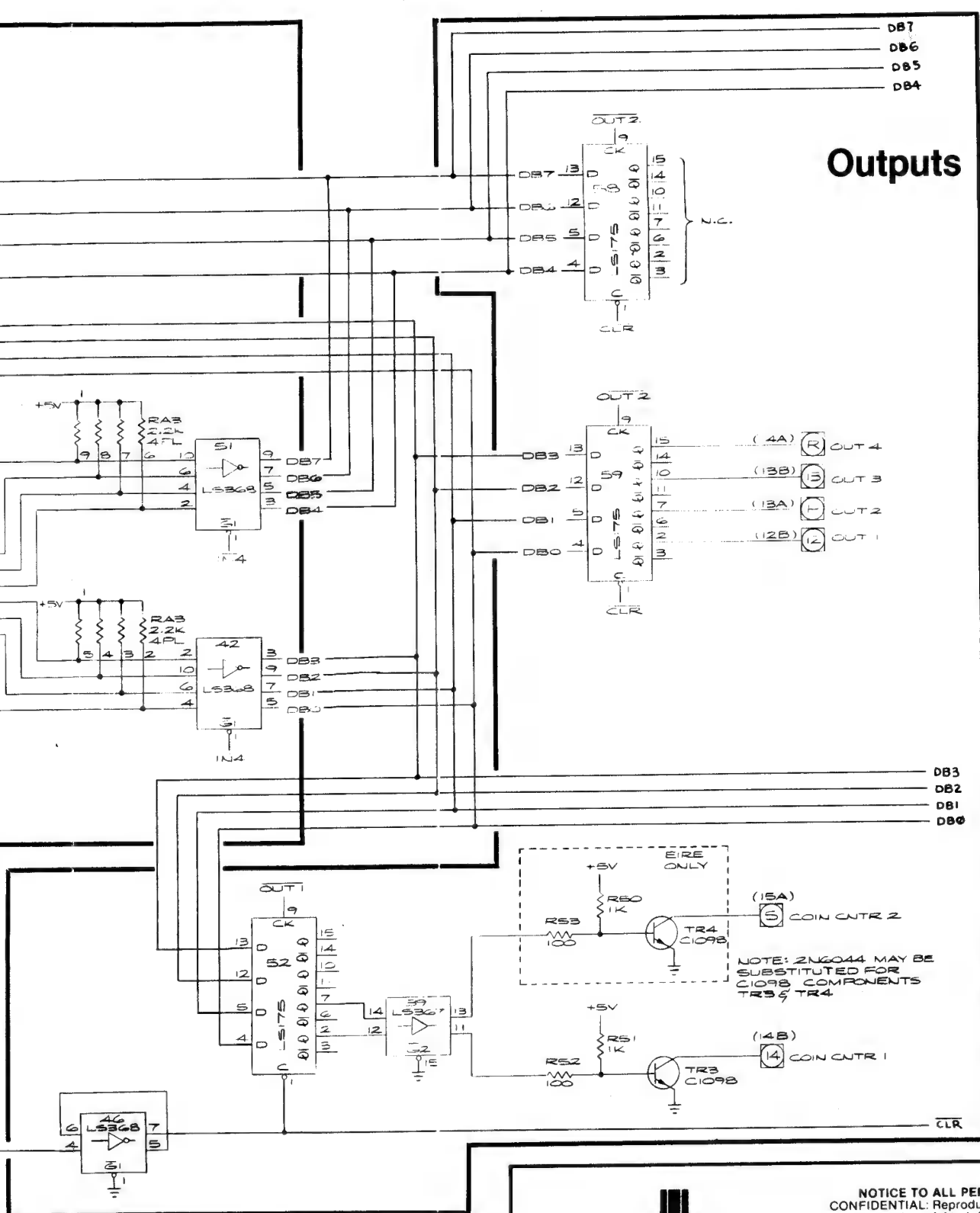
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Program ROM



RAM





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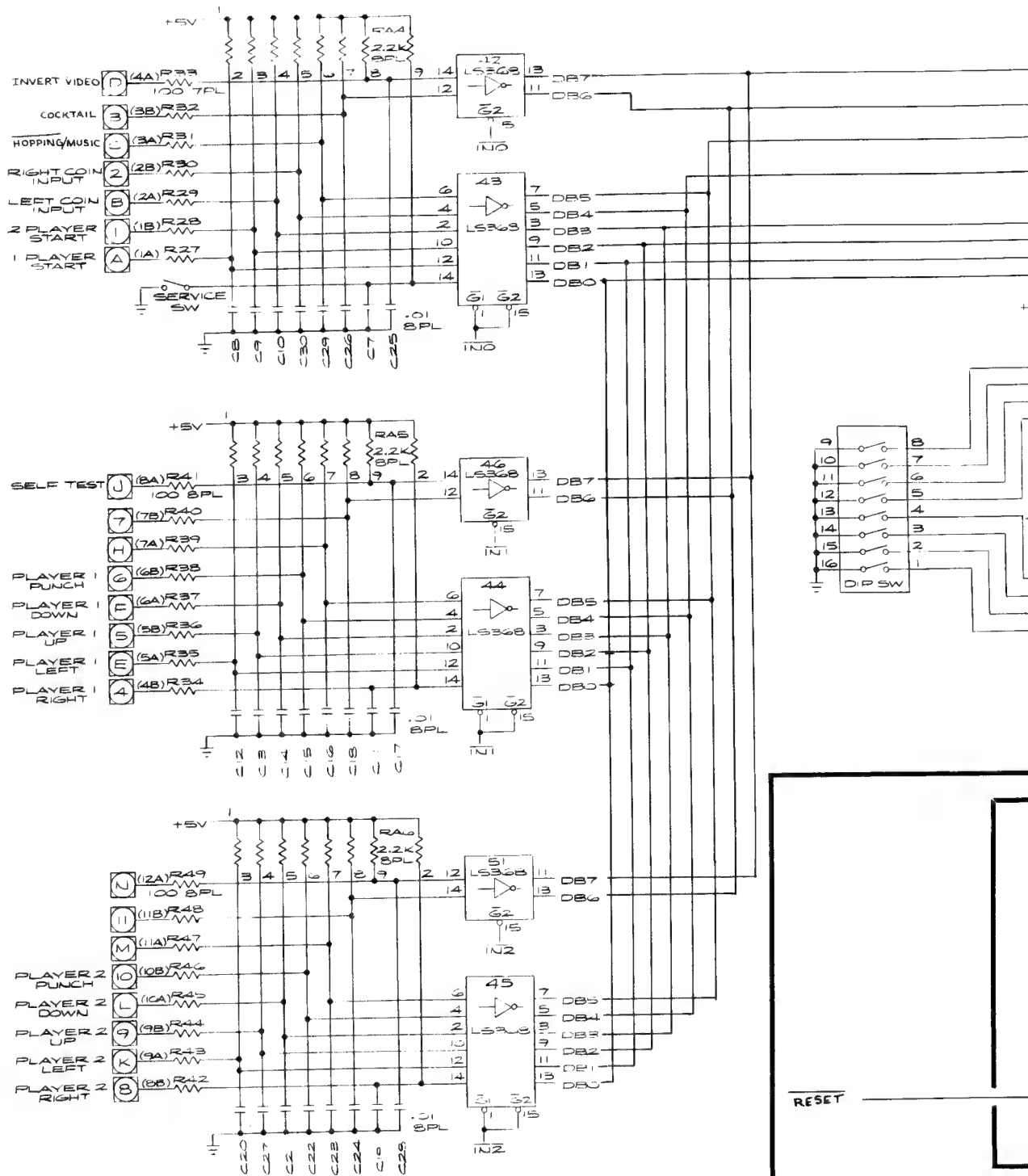
Kangaroo CPU PCB Schematic Diagram

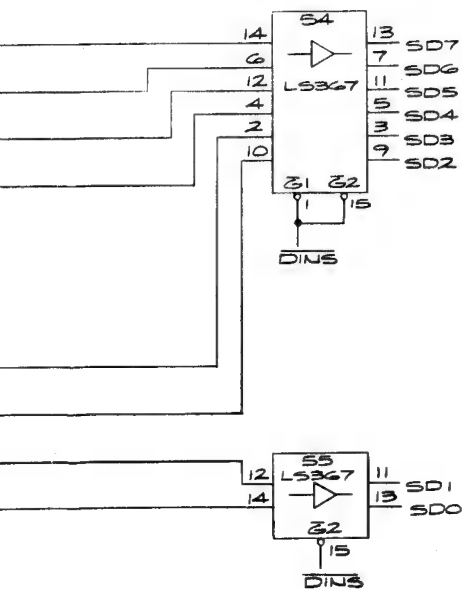
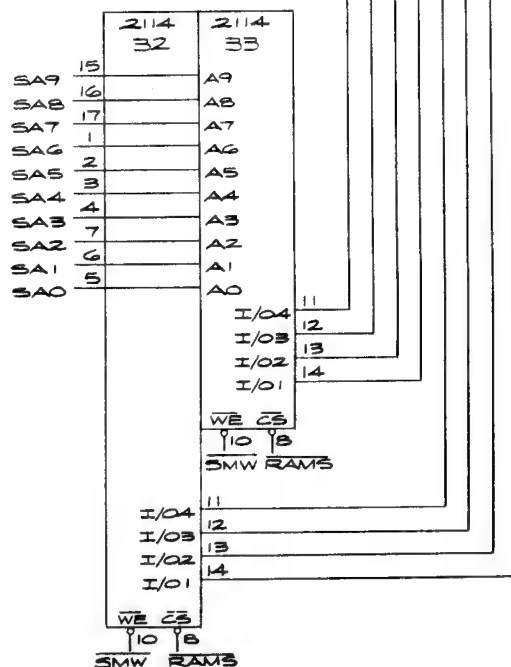
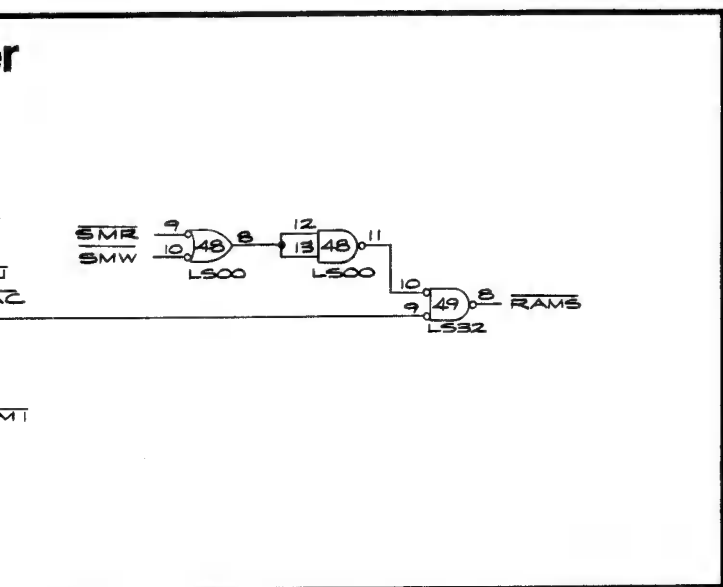
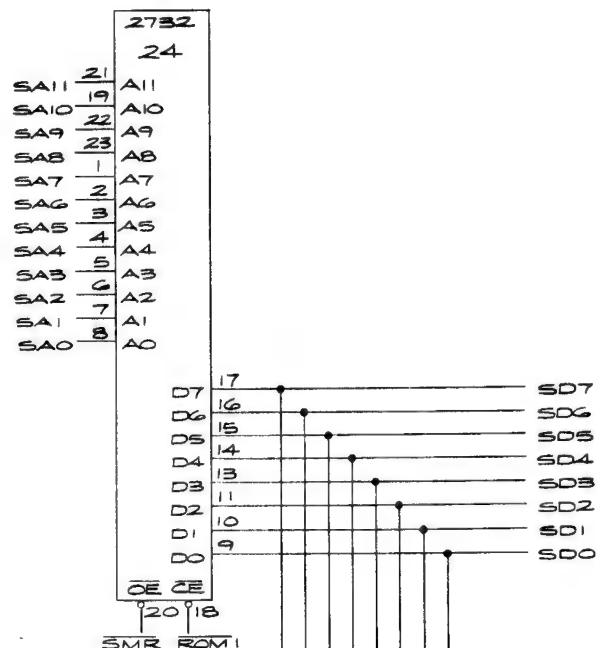
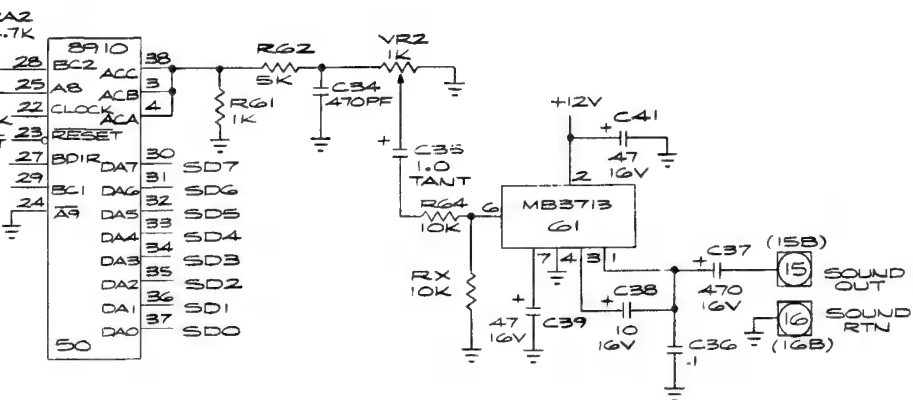
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Inputs





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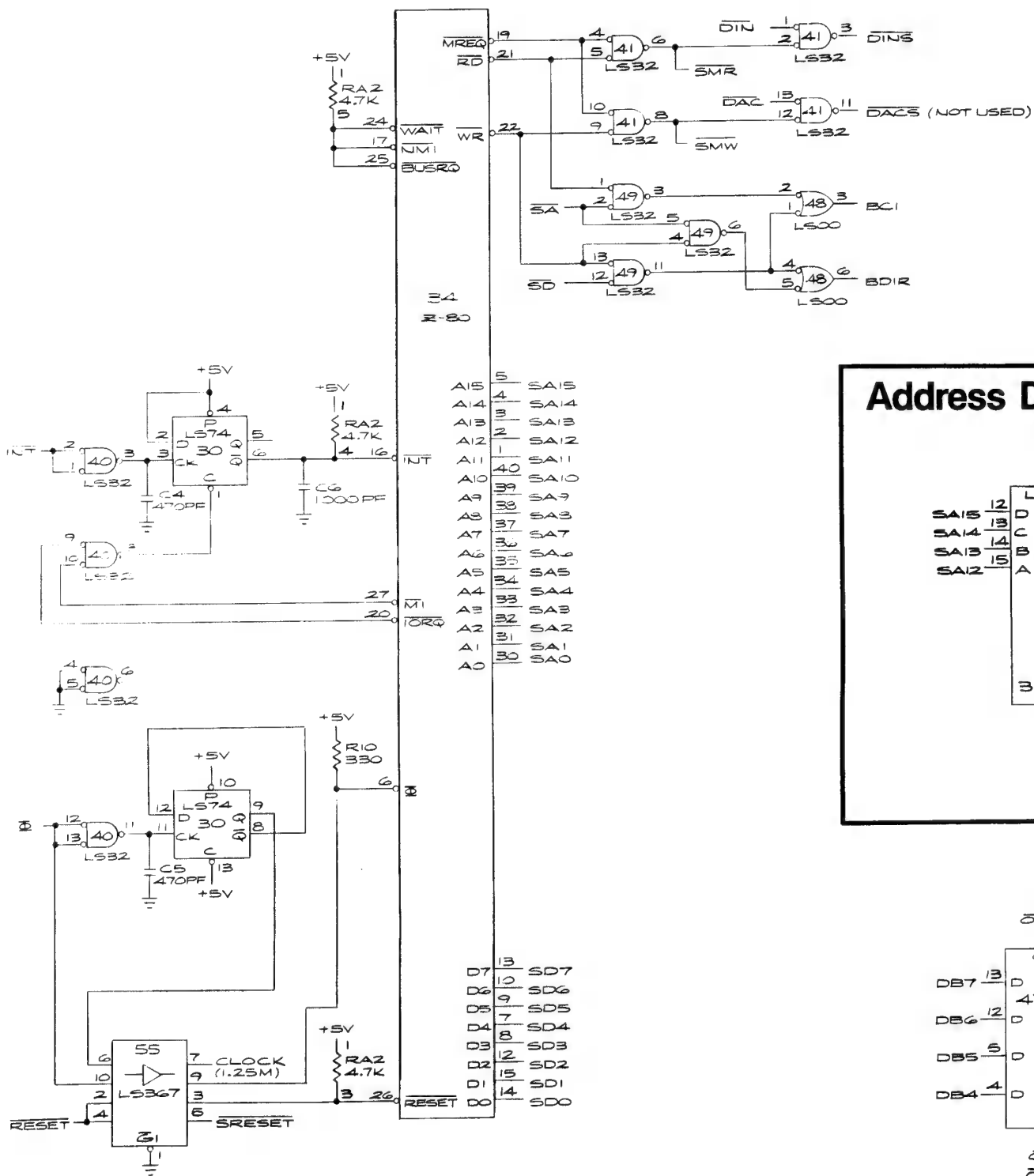
Kangaroo CPU PCB Schematic Diagram

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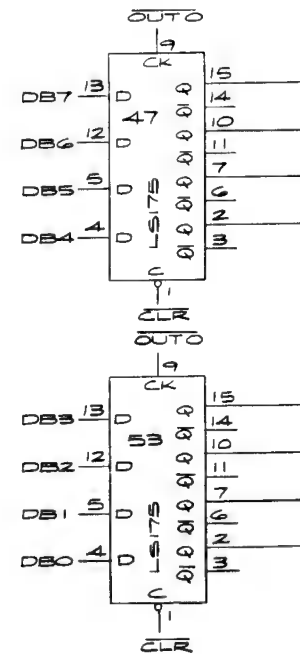
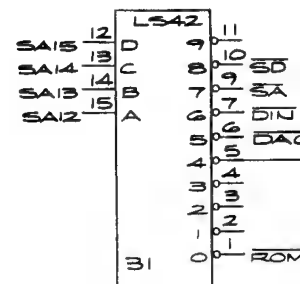
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Sound Microprocessor



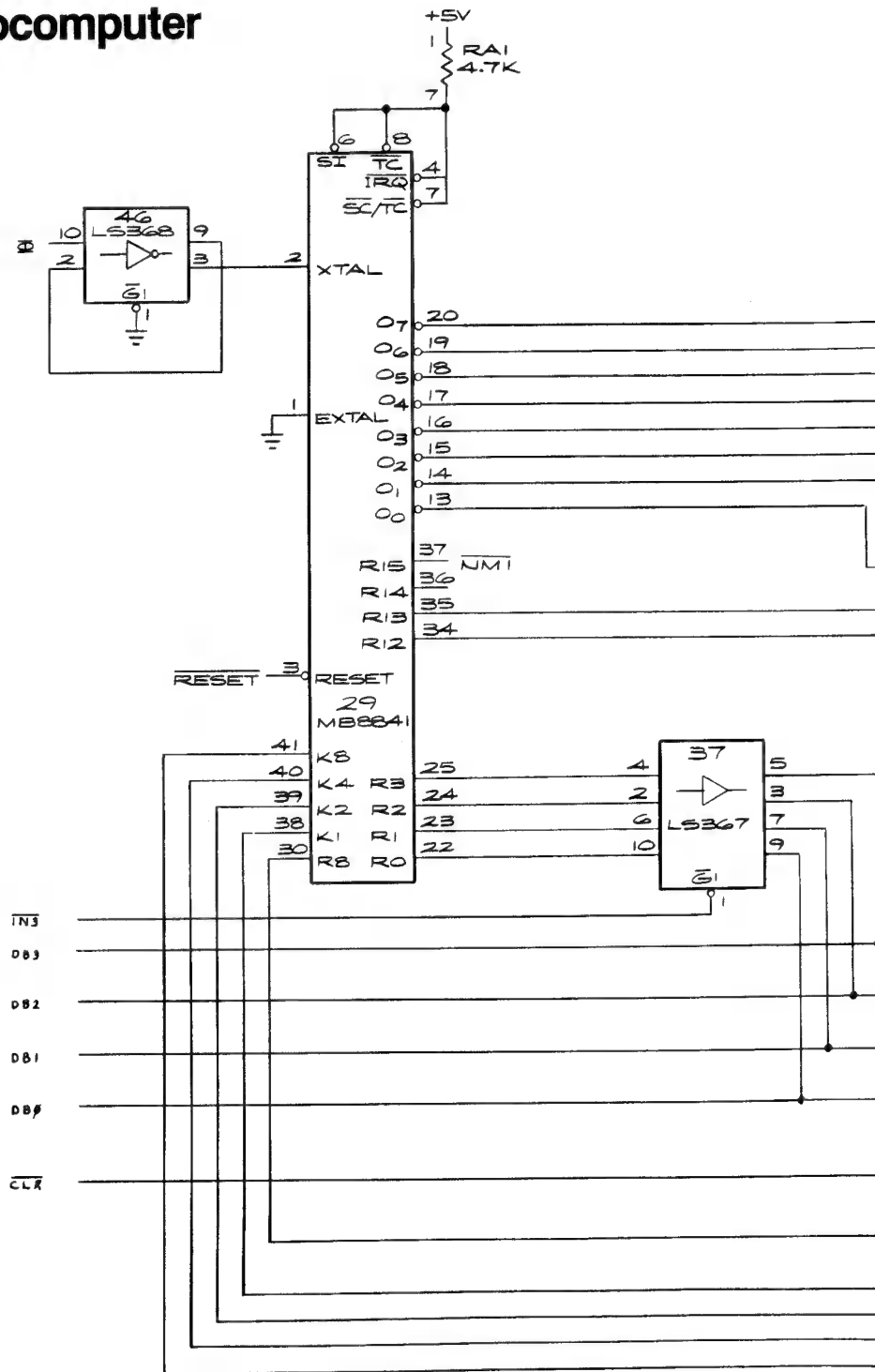
Address Decoder

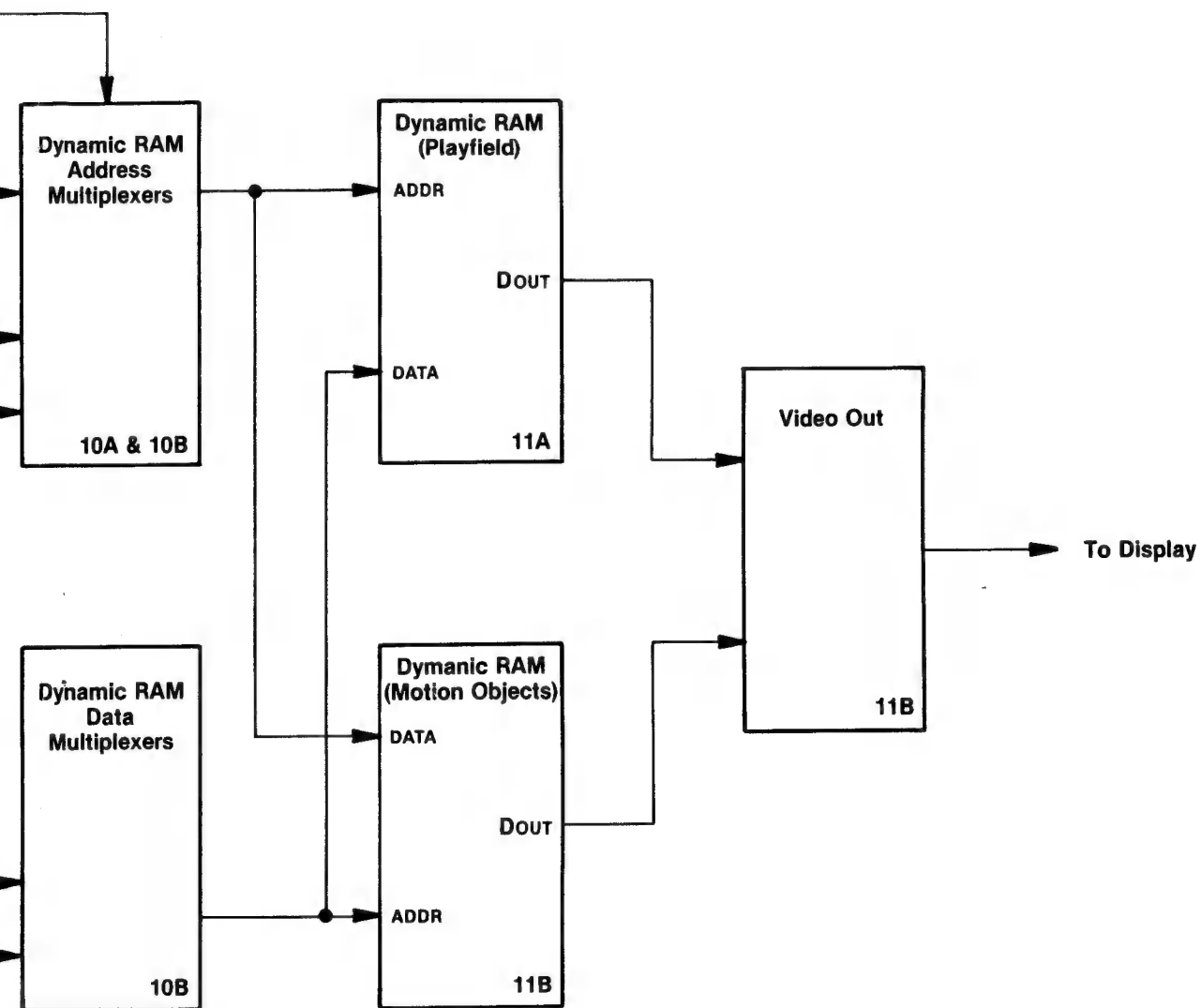




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Custom Microcomputer





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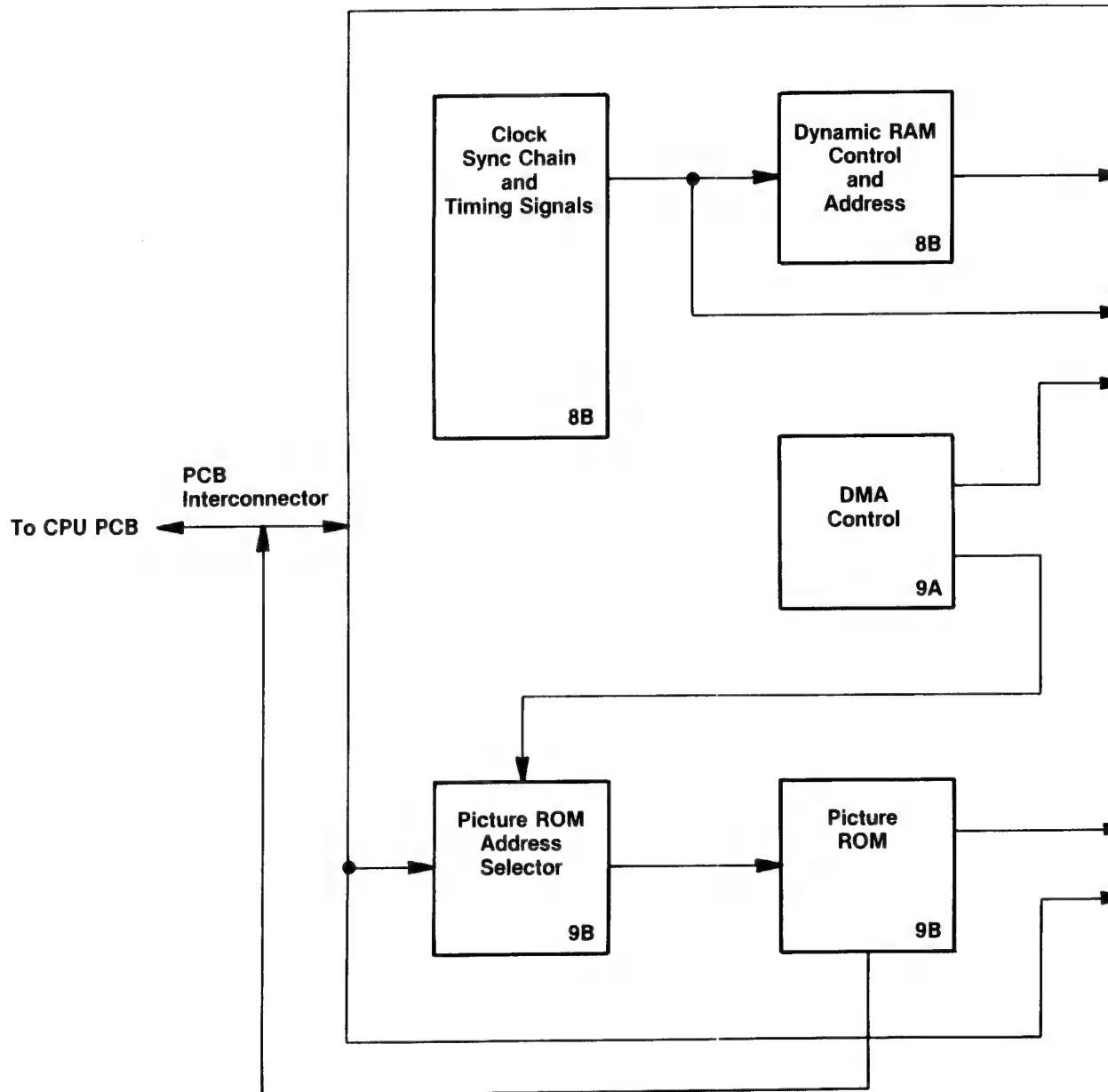
Kangaroo Video PCB Schematic Diagram

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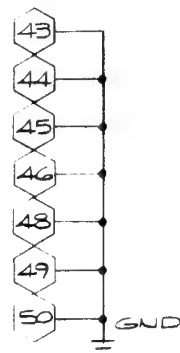
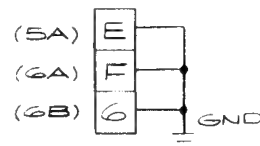
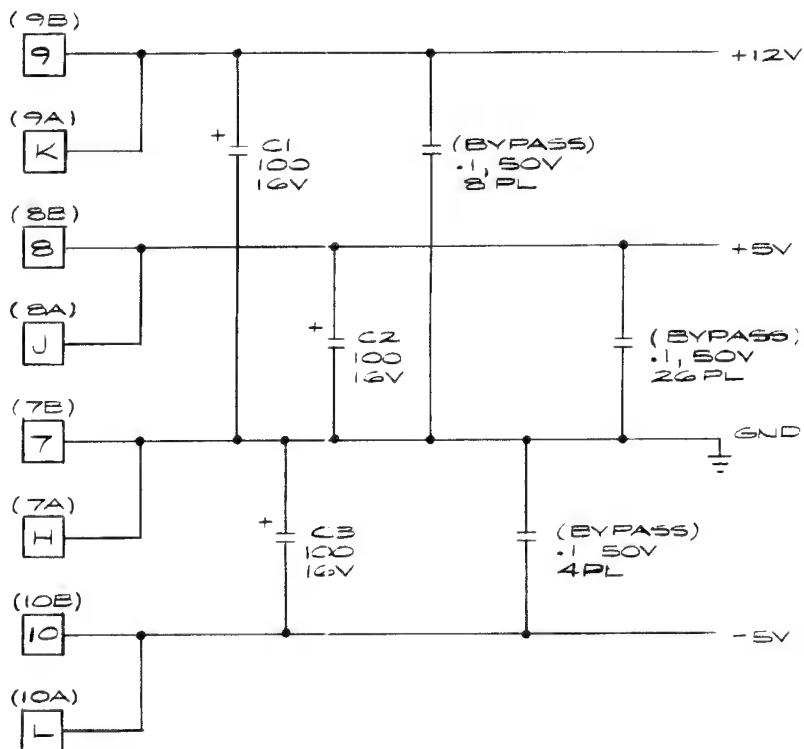
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Block Diagram (Video PCB)



Video Power Input



NOTES:

1.  = CONNECTOR CN1
2.  = CONNECTOR CN4



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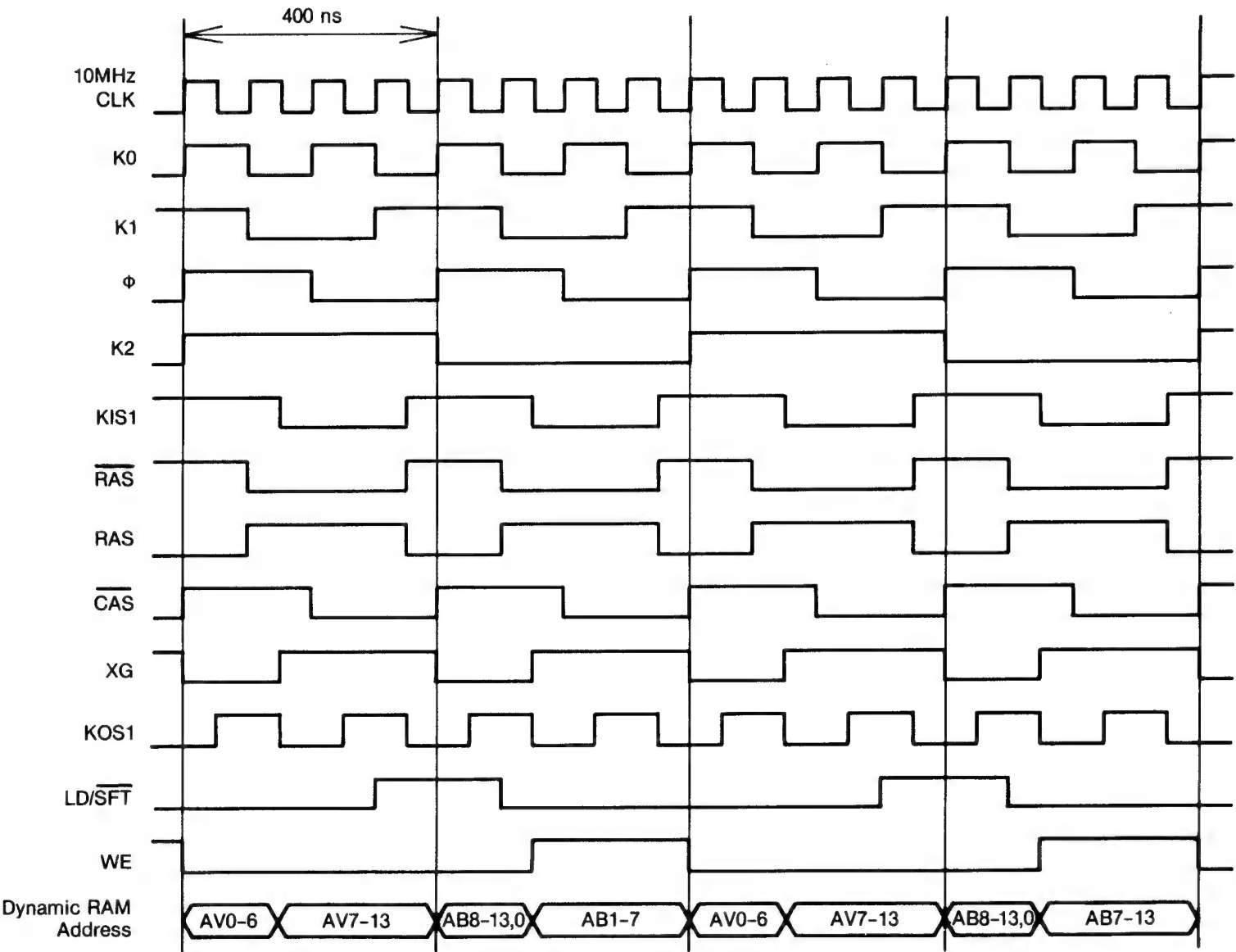
Kangaroo Video PCB Schematic Diagram

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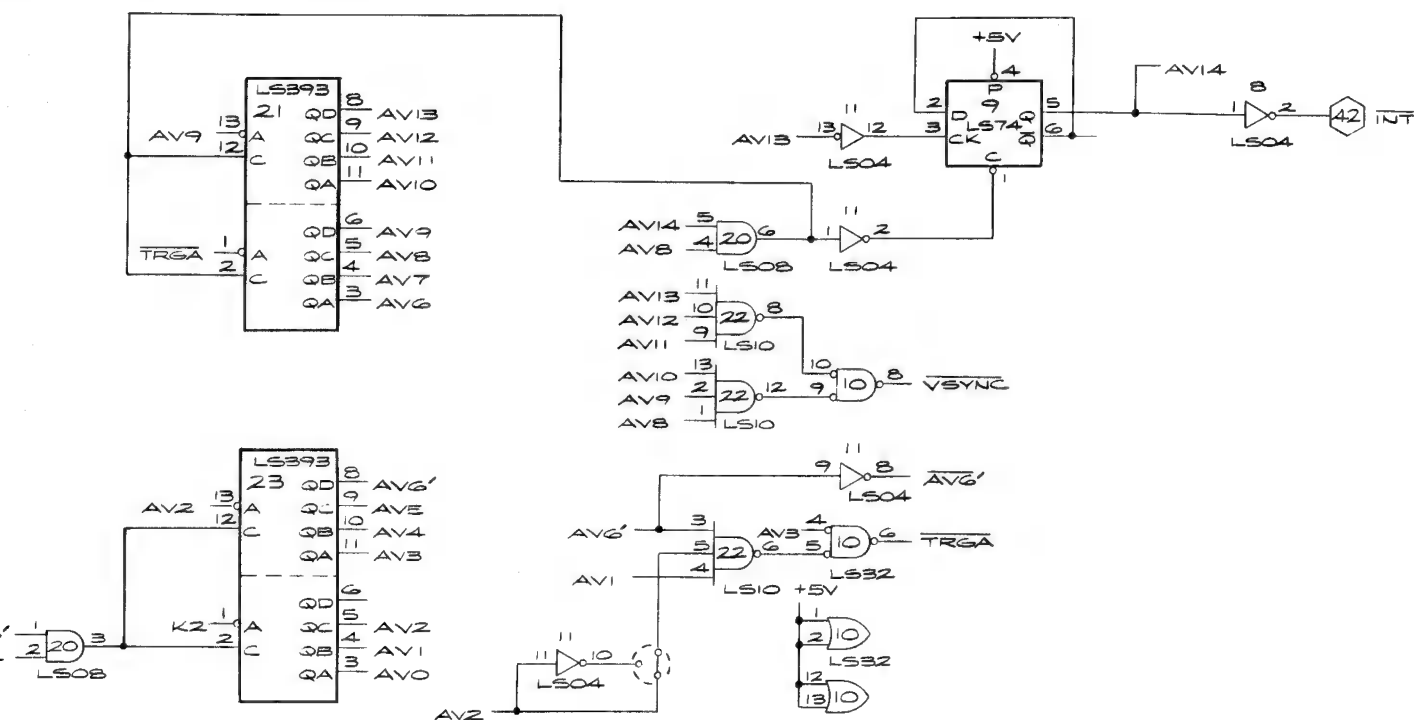
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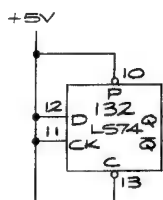
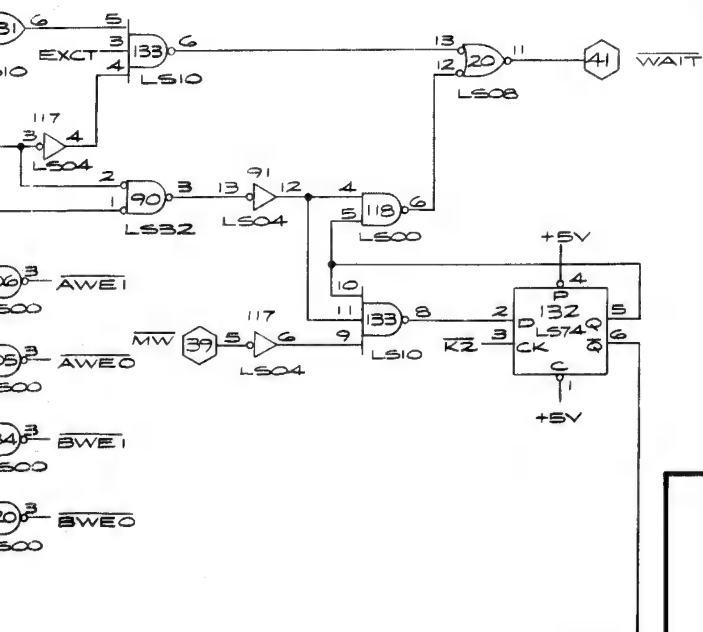
Dynamic RAM Timing Diagram (Video PCB)



and Timing Signals



Address Decoding



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Kangaroo Video PCB Schematic Diagram

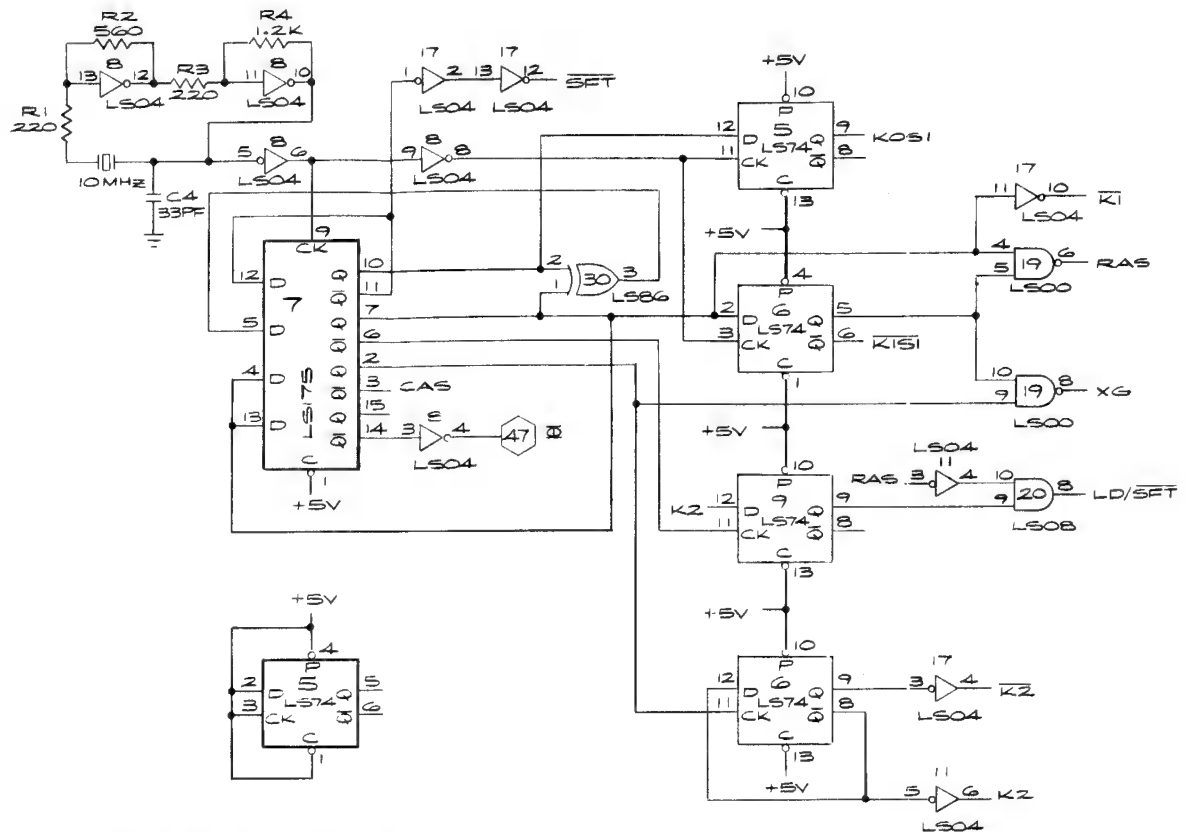
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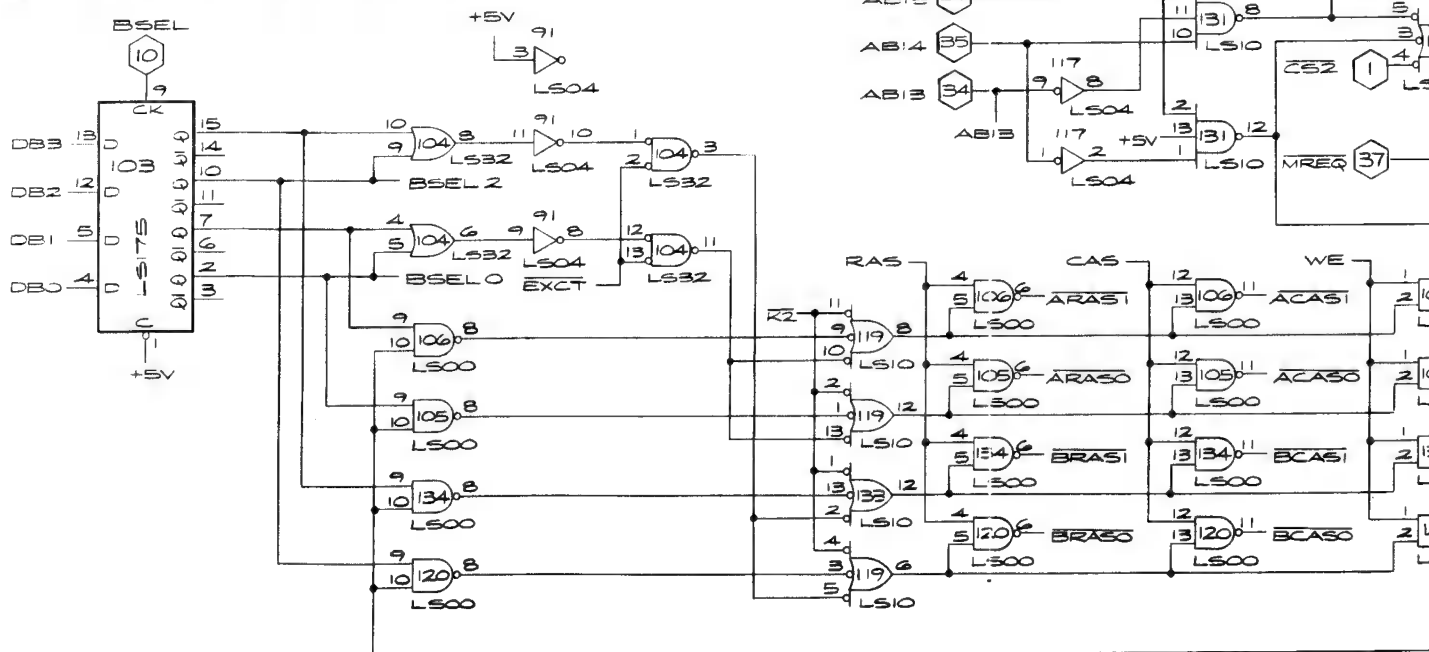
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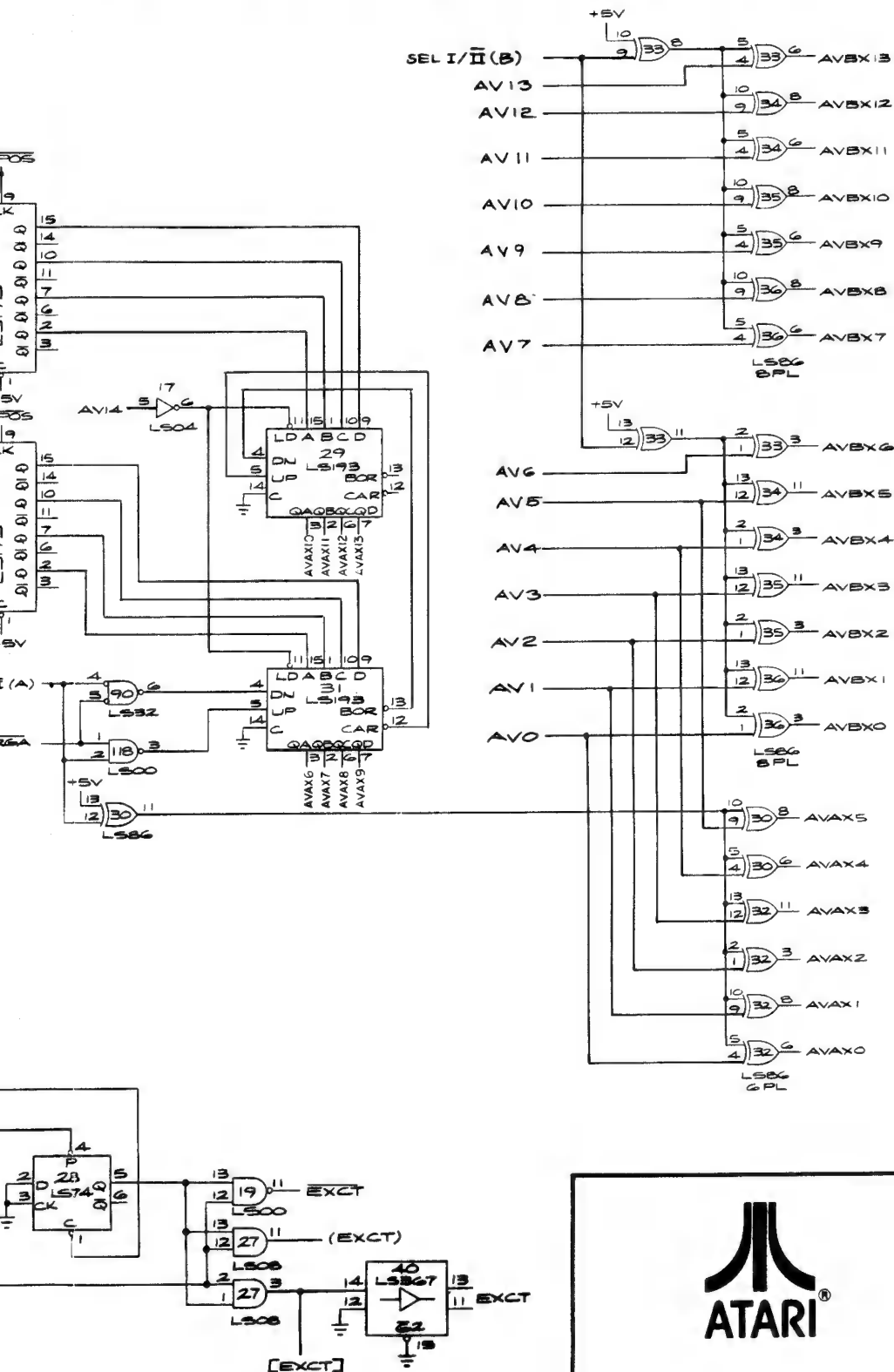
Clock, Sync Chain, a



Dynamic RAM Control



Dynamic RAM Video Address and Flip



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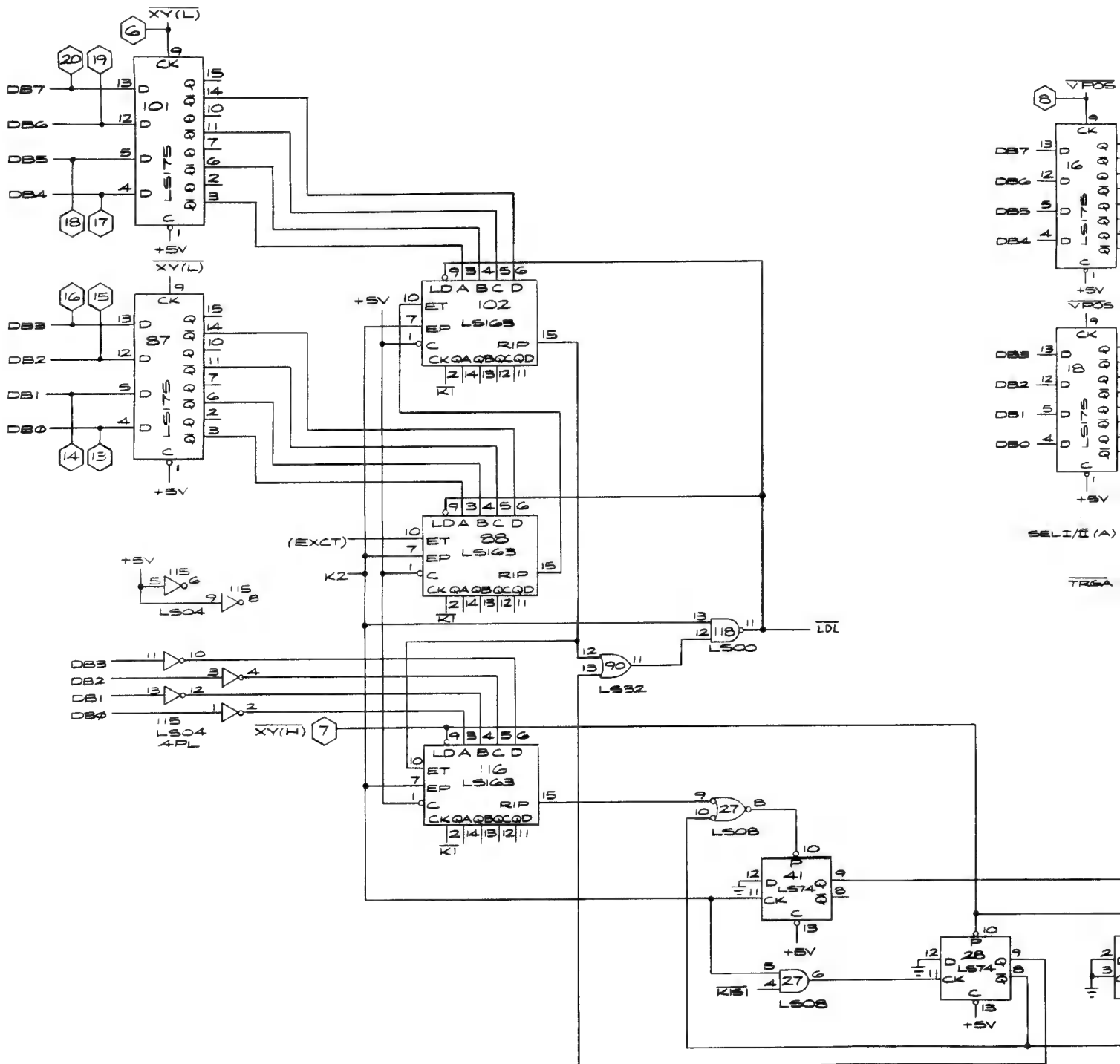
Kangaroo Video PCB Schematic Diagram

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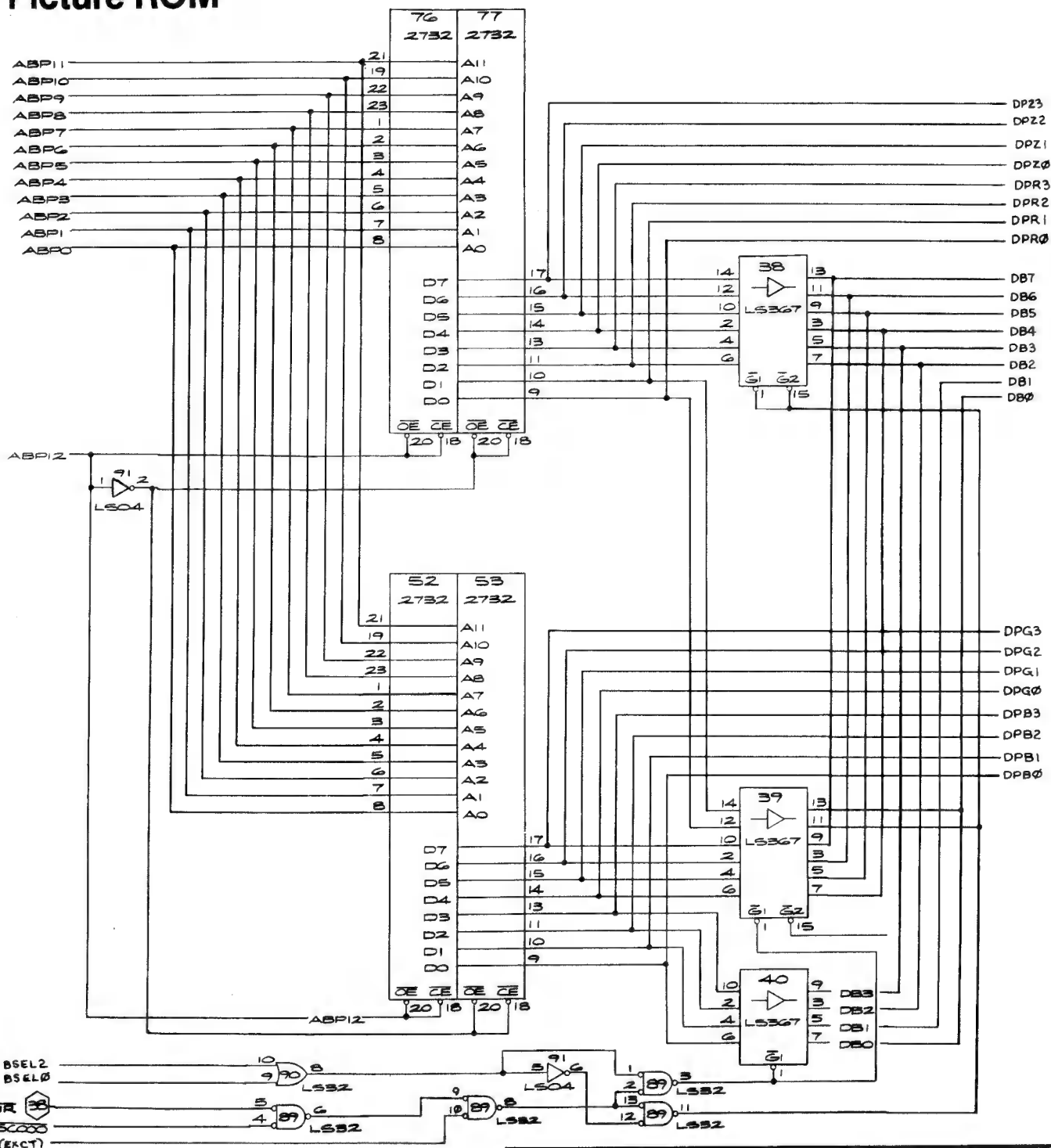
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DMA Control



Picture ROM



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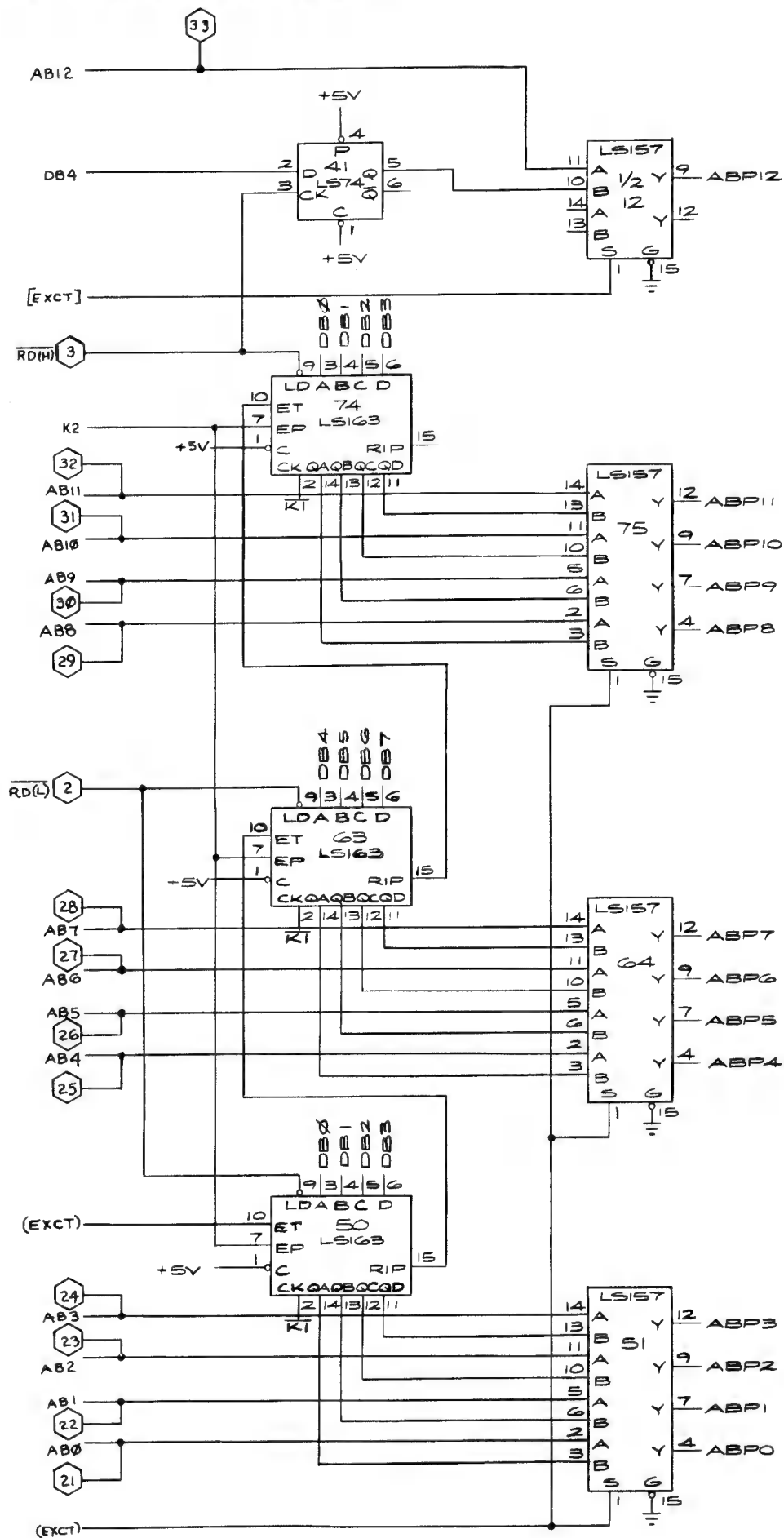
Kangaroo Video PCB Schematic Diagram

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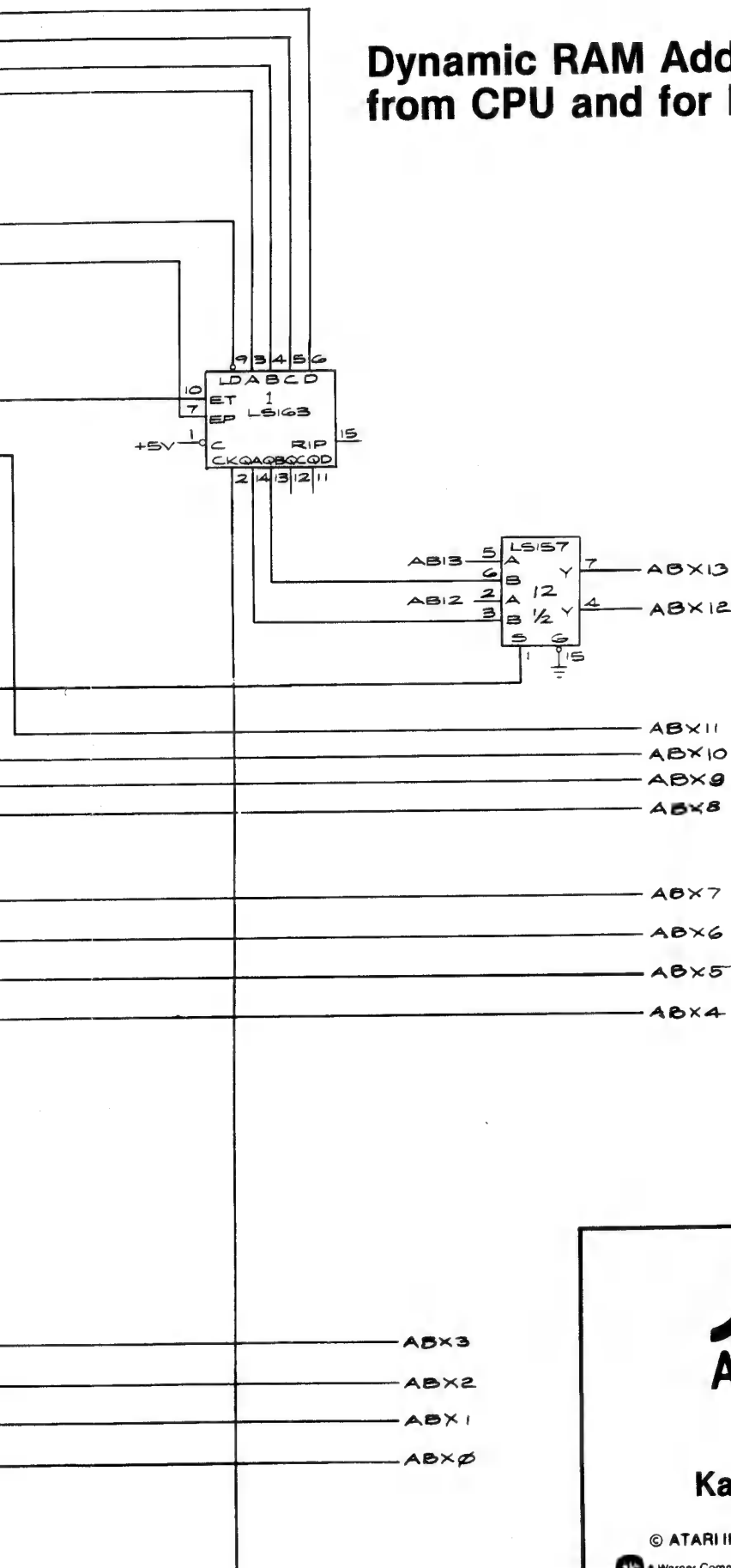
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SP-204 Sheet 9B
 3rd printing

Picture ROM Address Selector



Dynamic RAM Address from CPU and for DMA



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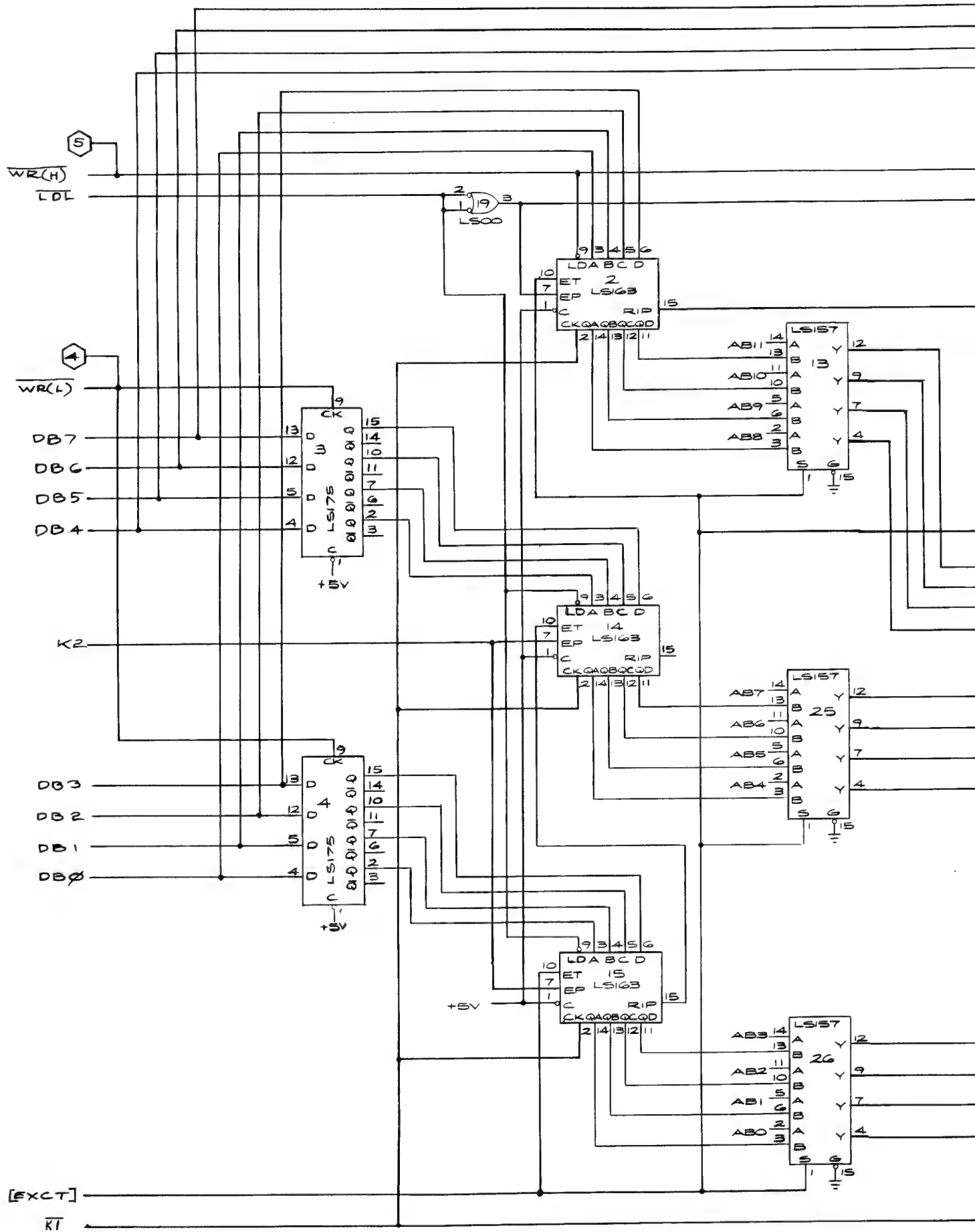
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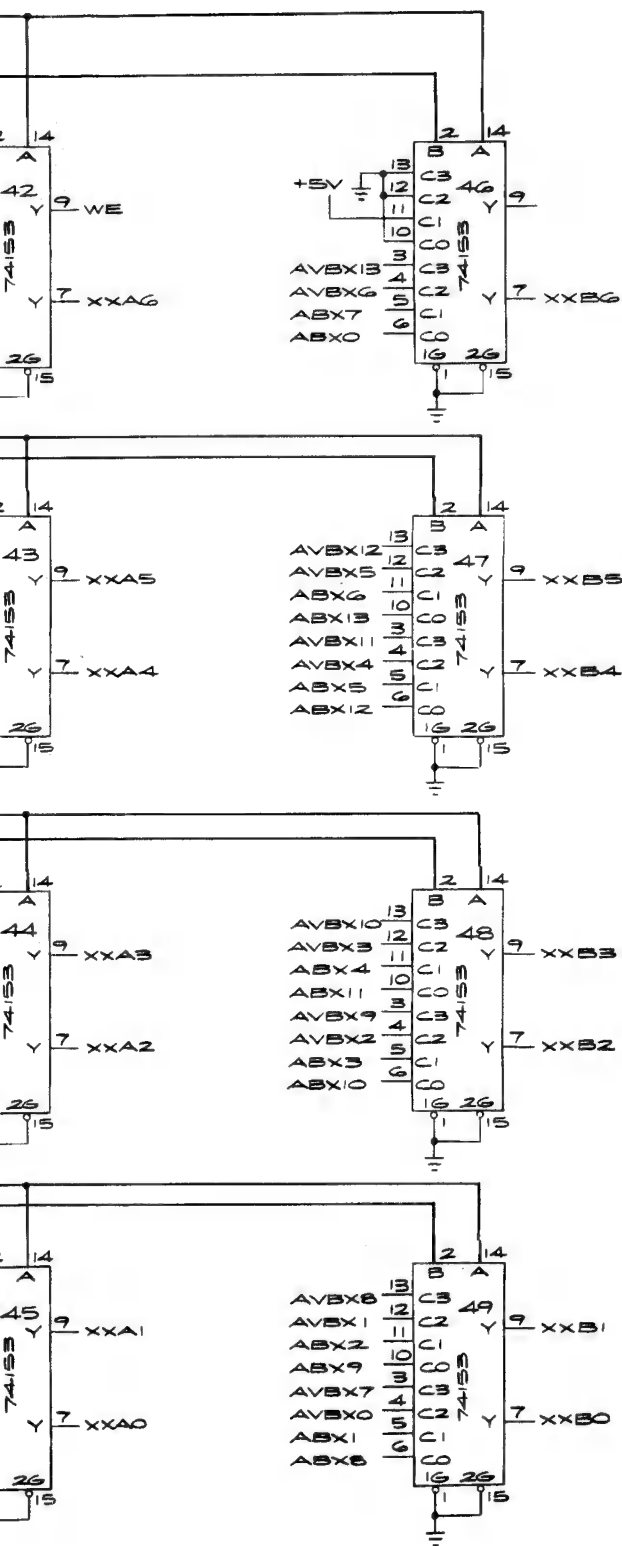


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Address Selector



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Kangaroo Video PCB Schematic Diagram

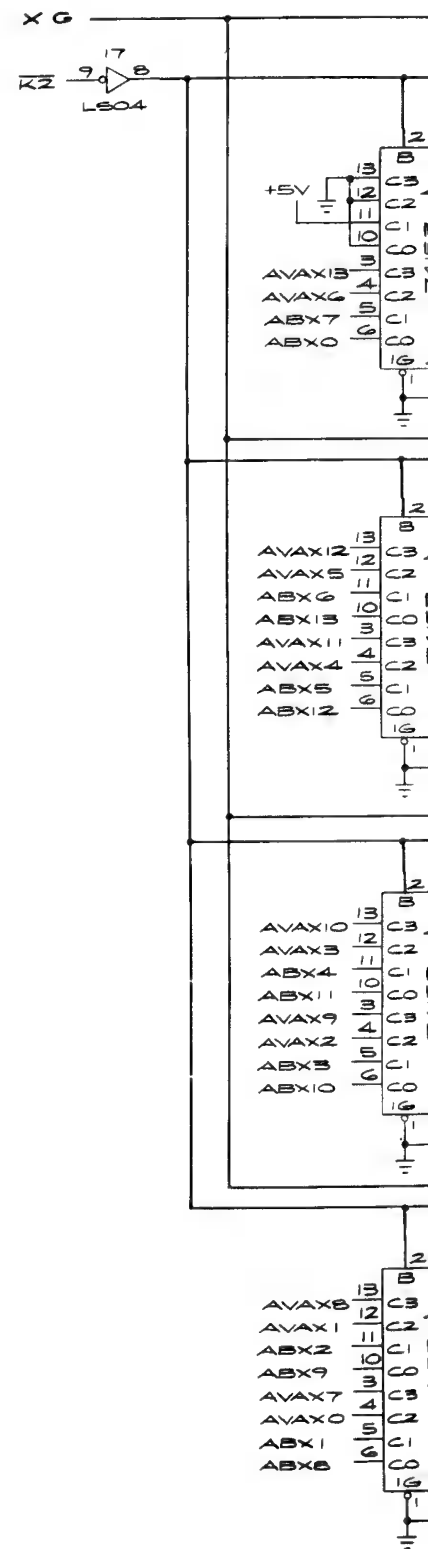
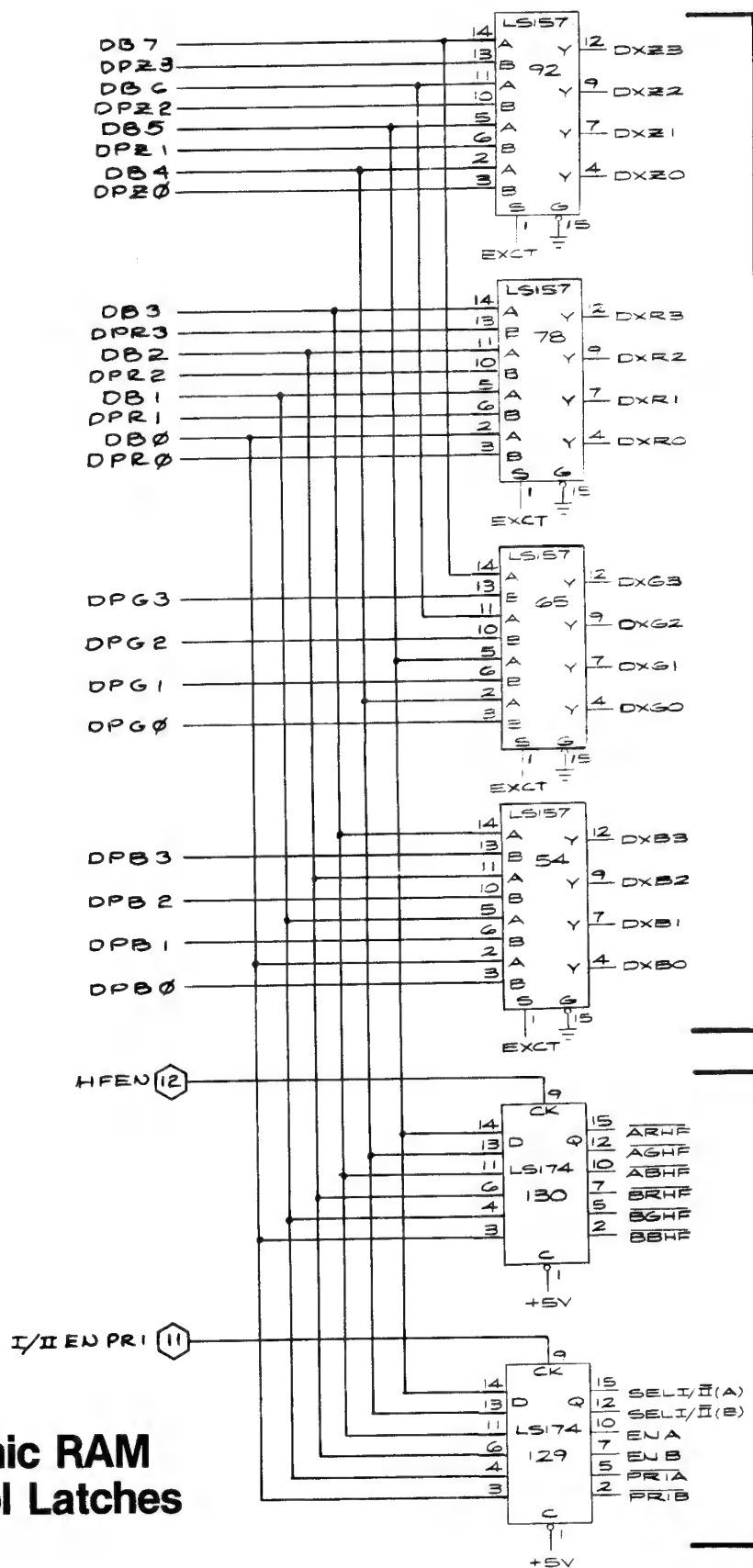
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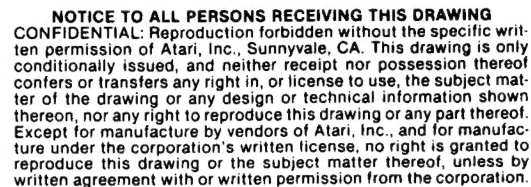
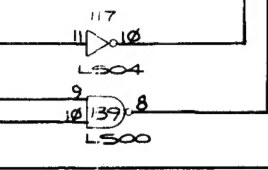
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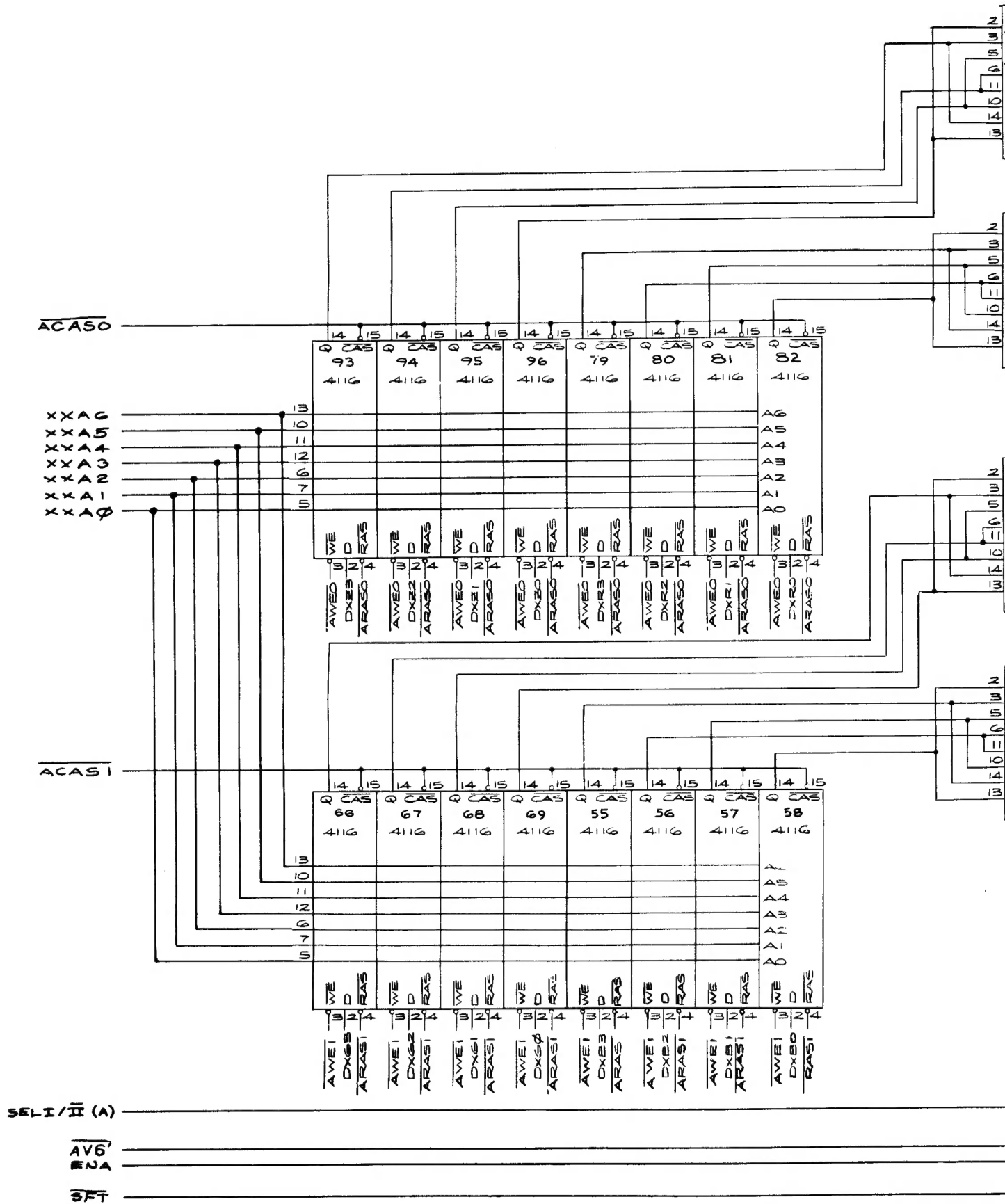
Dynamic RAM Data Selector

Dynamic RAM

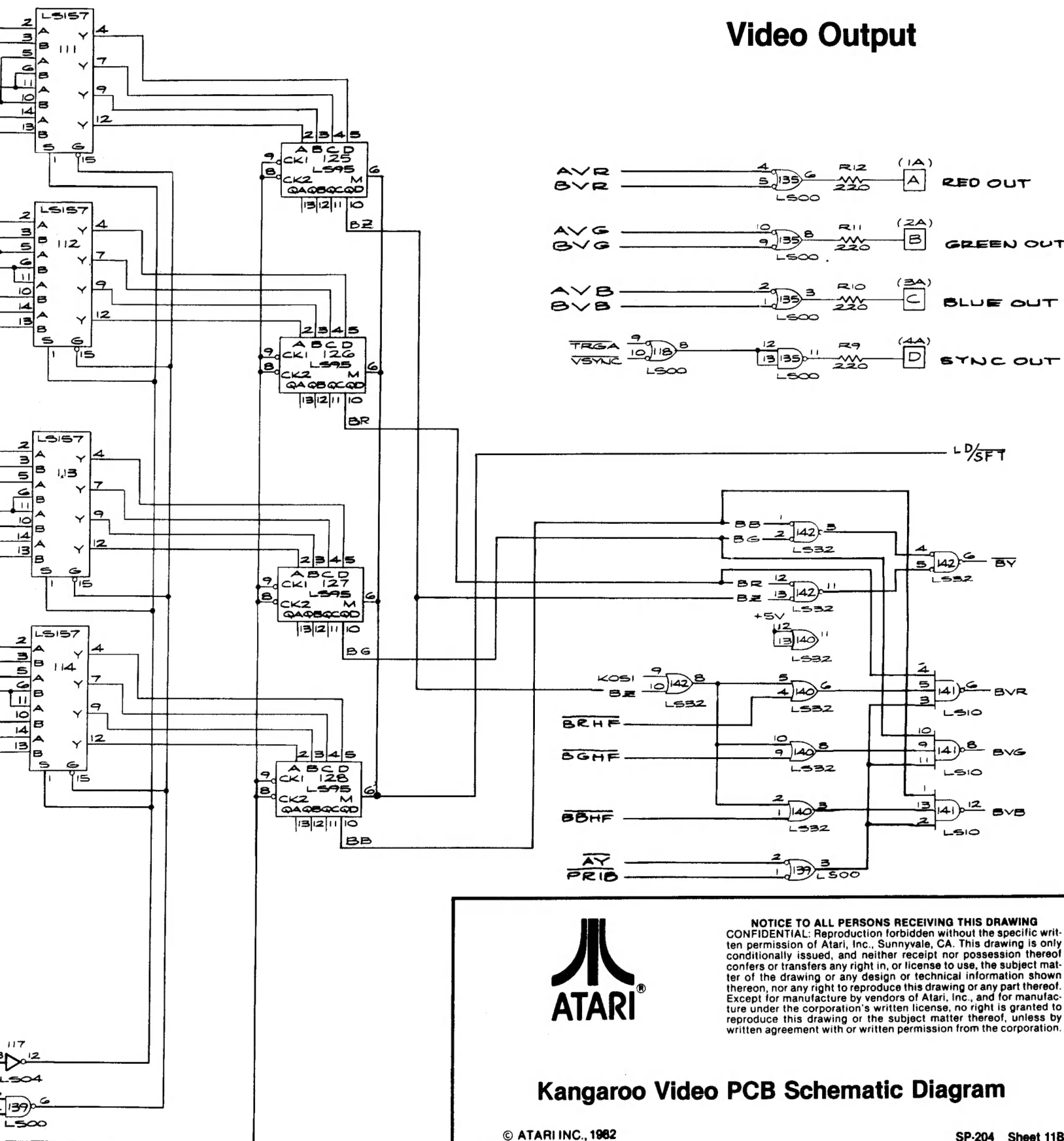




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Video Output



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Kangaroo Video PCB Schematic Diagram

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Motion Object (Dynamic RAM B)

